CALIFORNIA INSTITUTE OF TECHNOLOGY
PHYSICS MATHEMATICS AND ASTRONOMY DIVISION

Sophomore Physics Laboratory (PH005/105)

Analog Electronics Appendix

(Revision October 2011)

Appendix A

Decibels

A.1 Definition of Bel and Decibel

The bel[1] is defined as the logarithm in base ten of a power P normalized to a reference power P_r , i.e.

$$X\,\mathrm{B} = \log_{10} \frac{P}{P_r}\,. \tag{A.1}$$

The decibel is 10 bels

$$X\,\mathrm{dB} = 10\,X\,\mathrm{B} = 10\,\log_{10} \frac{P}{P_r}\,. \tag{A.2}$$

Bels and decibels are dimensionless. The bel is not a unit, but a formula to conveniently scale homogeneous quantities thanks to the properties of the logarithmic function.

Bels are not commonly used because if we consider integer values of bels they do not provide a good resolution. We will just consider decibels for the remainder of this notes.

Considering that

$$P = \frac{V^2}{|Z|} = |Z|\,I^2,$$

and supposing that we use the same reference impedance magnitude $|Z_r|$

[1]The units scale "bell" was name after Alexandre Graham Bell, scientist and inventor.

for P, and P_r,we can rewrite equ. (A.2) as

$$X\,\mathrm{dB} = 20\log_{10}\frac{V}{V_r} = 20\log_{10}\frac{I}{I_r},$$

where V_r, and I_r are respectively the voltage and the current across the reference impedance $|Z_r|$. In other words, we have to measure the voltage or the currents across equal impedances, to get the decibels.

A.2 Generalization of the Use of Decibel

For practical purposes, the decibel is also used to report the ratio of any homogeneous quantities such as the voltage output V_o over the voltage input V_i of a two port network, or in general, the ratio of any kind of homogeneous quantities x_1, x_2

$$X\,\mathrm{dB} = 20\log_{10}\frac{x_1}{x_2}\,.$$

In this case there is no normalization respect to a reference load R_r or power P_r.

A.3 Useful Table and Properties

The next table is quite useful to easily translate decibels into magnitude

[dB]	**0**	1	2	3	4	5	**6**	7	8	9	**10**
Magnitude	**1**	1.1	1.2	1.4	1.6	1.8	**2**	2.2	2.5	2.8	**3.2**

For convenience, letŽs rewrite some useful properties of the logarithm function

$$\begin{aligned}
\log(x\,y) &= \log\,x + \log\,y, \\
\log(x/y) &= \log\,x - \log\,y, \\
\log x^n &= n\log\,x, \\
\log_a x &= \log_b x/\log_b a.
\end{aligned}$$

A.4 Standard Power References

Decibels comes in many flavors (different reference powers) depending on the application, radio frequency, microwaves, optics, acoustics, et cetera.

For example the following definition is quite often used

$$X\,\mathrm{dBm}(R_r) = 10\log_{10}\frac{V^2/R}{1\mathrm{mW}}$$

The value of R_r depends on the application field

	R_r [Ω]
Radio Frequency	50
TV Frequencies	75
Audio Frequencies	600

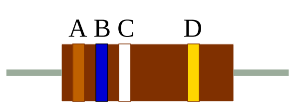
Appendix B

Resistor Color Code

Nominal values of resistances are coded using colors bands around the resistors (see figure below). The bands identify digits and the exponent in base ten for the resistance value and the tolerance as explained in the following table:

Band Number	1	2	3	4 (Tolerance band)	5
3 Bands	Digit	Digit	Exponent	Always 20%	
4 Bands	Digit	Digit	Exponent	Tolerance	
5 Bands	Digit	Digit	Exponent	Tolerance	Tolerance after 1000 hours

3 Band resistors have no band for the tolerance because it is assumed to be 20% of the nominal values.The fifth band is not an industry standard, but quite often it means the tolerance after 1000 hours of continuous use.



$$R = AB \cdot 10^C, \quad \Delta R = R \cdot D$$

The bands are counted from left to right. The following table reports the coding of the values using colors and a mnemonic sentence to remember the color code table.

Mnemonic Sentence	Color	Exponent	Tolerance (%)	Tolerance (%) 5th Band
Big	Black	0	20	
Bart	Brown	1	1	1%
Rides	Red	2	2	0.1%
Over	Orange	3		0.01%
Your	Yellow	4		0.001%
Grave	Green	5		
Blasting	Blue	6		
Violent	Violet	7		
Guns	Gray	8		
Wildly.	White	9		
Go	Gold	-1	5	
Shoot (him?)	Silver	-2	10	

For example, the nominal resistance of a 4 band resistor having the sequence brown, black, orange and gold is

$$\begin{aligned} R_{nom.} &= 10\,\mathrm{k}\Omega \\ \Delta R_{nom.} &= 5\%10\,\mathrm{k}\Omega \end{aligned} \quad \Rightarrow \quad R_{nom.} = (10.0 \pm 0.5)\ \mathrm{k}\Omega$$

Resistor size (volume) is related to the power dissipation capability. Typical used values are 1/8W, 1/4W, 1/2W, 1W.

Appendix C

The Cathode Ray Tube Oscilloscope

Every time we need to analyze or measure an electronic signal in the time domain we will probably use some type of oscilloscope. The oscilloscope is therefore one of the most useful tools used in a laboratory. Practically, it is an indispensable instrument for measuring, designing, manufacturing, or repairing electronic equipment.

Quite often, one can still find old Cathode Ray Tube (CRT) oscilloscopes even in modern laboratory, mainly because of the inadequacy of state of the art digital oscilloscope scopes to represent very fast signals. It is therefore worthwhile to study this device and understand how a CRT works and also its limitations.

C.1 The Cathode Ray Tube Oscilloscope

The *cathode ray tube oscilloscope* is essentially an analog[1] instrument that is able to measure time varying electric signals. It is made of the following functional parts (see figure C.1):

- the cathode ray tube (CRT),
- the trigger,

[1]Hybrid instruments combining the characteristics of digital and analog oscilloscopes, with a CRT, are also commercially available.

- the horizontal input,
- the vertical input,
- time base generator.

Let's study in more detail each component of the oscilloscope.

C.1.1 The Cathode Ray Tube

The CRT is a vacuum envelope hosting a device called *an electron gun* , capable of producing an electron beam, whose transverse position can be modulated by two electric signals (see figures C.1 and C.7).

When the electron gun cathode is heated by wire resistance because of the Joule effect it emits electrons . The increasing voltage differences between a set of shaped anodes and the cathode accelerates electrons to a terminal velocity v_0 creating the so called electron beam.

The beam then goes through two orthogonally mounted pairs of metallic plates. Applying voltage difference to those plates V_x and V_y, the beam is deflected along two orthogonal directions (x and y) perpendicular to its direction z. The deflected electrons will hit a plane screen perpendicular to the beam and coated with florescent layer. The electrons interaction with this layer generates photons, making the beam position visible on the screen.

C.1.2 The Horizontal and Vertical Inputs

The vertical and horizontal plates are independently driven by a variable gain amplifier to adapt the signals $v_x(t)$, and $v_y(t)$ to the screen range. A DC offset can be added to each input to position the signals on the screen. These two channels used to drive the signals to the plates signals are called horizontal and vertical inputs of the oscilloscope.

In this configuration the oscilloscope is an x-y plotter.

C.1.3 The Time base Generator

If we apply a sawtooth signal $V_x(t) = \alpha t$ to the horizontal input, the horizontal screen axis will be proportional to time t. In this case a signal $v_y(t)$

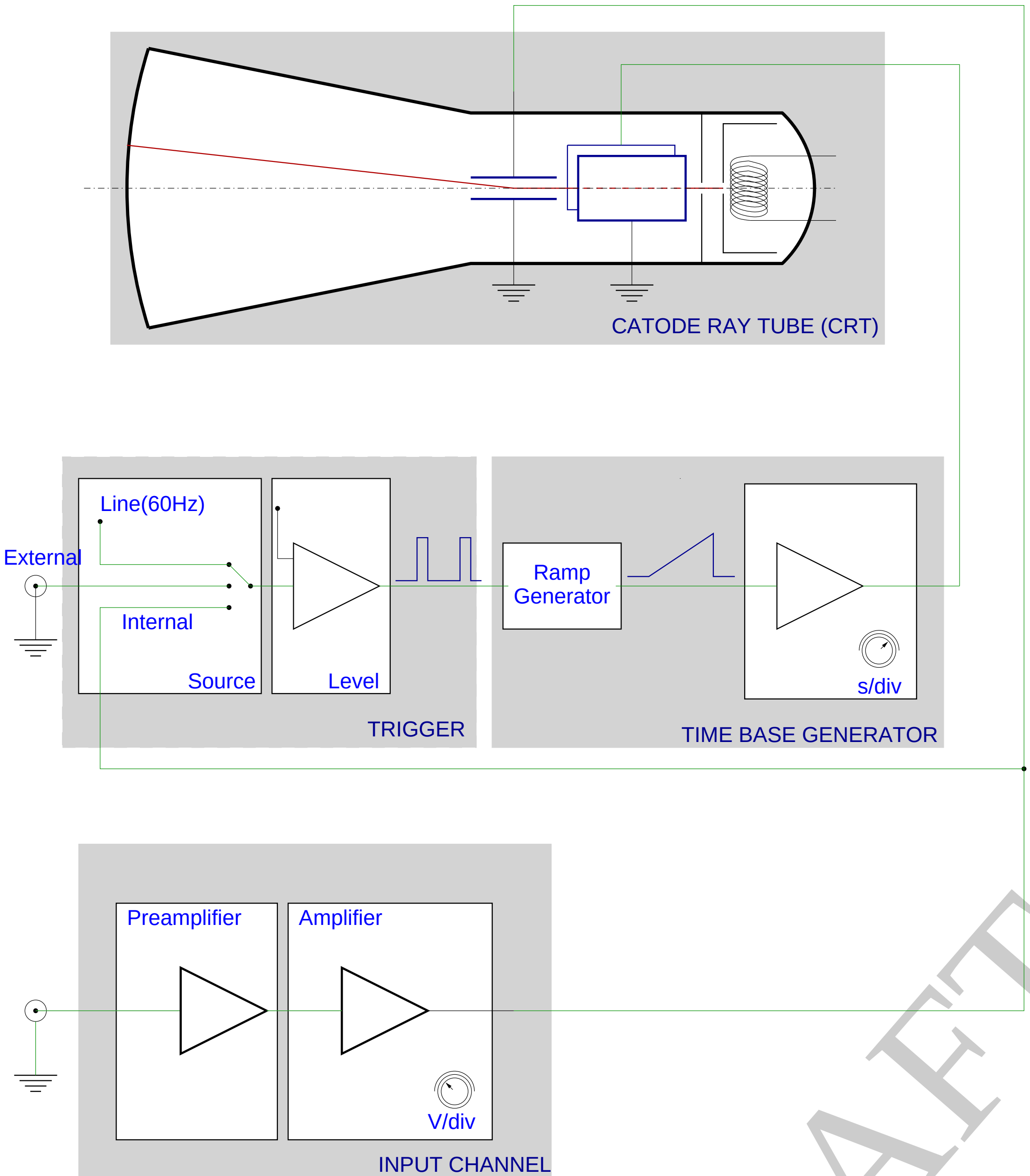


Figure C.1: Sketch of the functional parts of the analog oscilloscope, preamplifier, amplifier, trigger, time base generator, and CRT.

applied to the vertical input, will depict on the oscilloscope screen the signal time evolution.

The internal ramp signal is generated by the instrument with an amplification stage that allows changes in the gain factor α and the interval of time shown on the screen. This amplification stage and the ramp generator are called the *time base generator.*

In this configuration, the horizontal input is used as a second independent vertical input, allowing the plot of the time evolution of two signals.

Visualization of signal time evolution is the most common use of an oscilloscope.

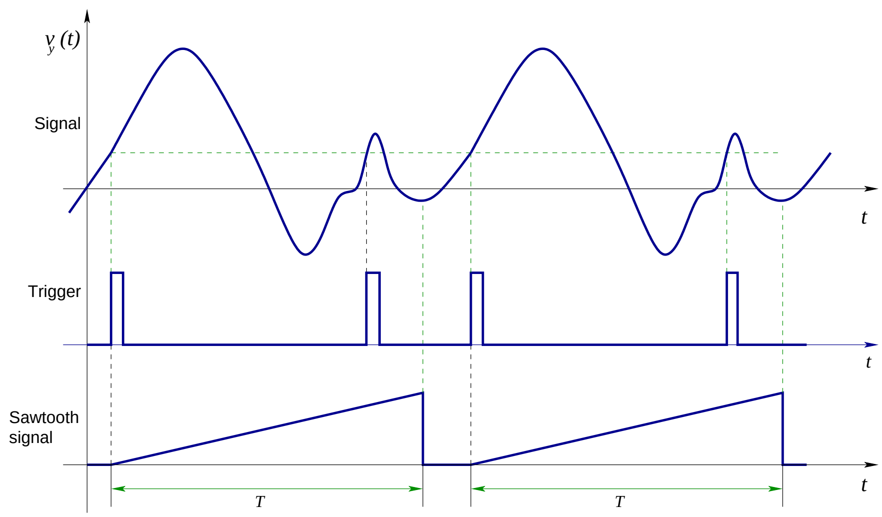


Figure C.2: Periodic Signal triggering.

C.1.4 The Trigger

To study a periodic signal $v(t)$ with the oscilloscope, it is necessary to synchronize the horizontal ramp $V_x = \alpha t$ with the signal to obtain a steady plot of the periodic signal. The trigger is the electronic circuit which provides this function. Let's qualitatively explain its behavior.

The trigger circuit compares $v(t)$ with a constant value and produces a pulse every time the two values are equal and the signal has a given slope. The first pulse triggers the start of the sawtooth signal of period[2] T

[2]In general, the sawtooth signal period T and the period of $v(t)$ are not equal.

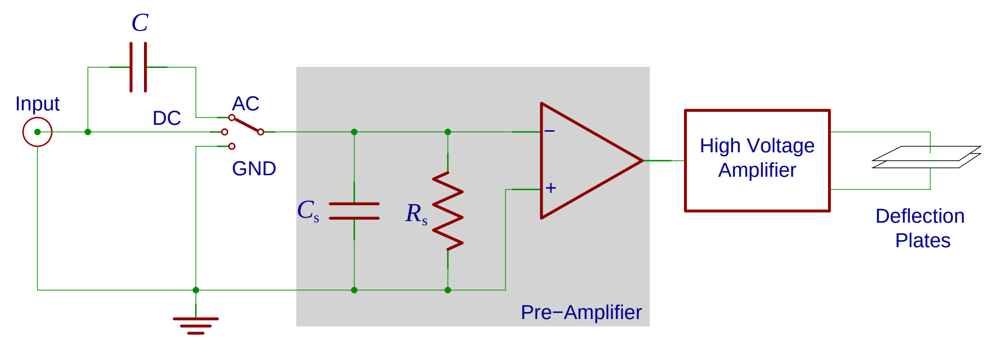


Figure C.3: Oscilloscope input impedance representation using ideal components (gray box). Input channel coupling is also shown.

, which will linearly increase until it reaches the value $V = \alpha T$, and then is reset to zero. During this time, the pulses are ignored and the signal $v(t)$ is plotted for a duration time T. After this time, the next pulse that triggers the sawtooth signal will happen for the same previous value and slope sign of $v(t)$, and the same portion of the signal will be re-plotted on the screen.

C.2 Oscilloscope Input Impedance

A good approximation of the input impedance of the oscilloscope is shown in the circuit of figure C.3. The different input coupling modes (DC AC GND) are also represented in the circuit.

The amplifying stage is modeled using an ideal amplifier (infinite input impedance) with a resistor and a capacitor in parallel to the amplifier input.

The switch allows to ground the amplifier input and indeed to vertically set the origin of the input signal (GND position), to directly couple the input signal (DC position), or to mainly remove the DC component of the input signal (AC position).

C.3 Oscilloscope Probe

An oscilloscope probe is a device specifically designed to minimize the capacitive load and maximize the resistive load added when the instrument is connected to the circuit. The price to pay is an attenuation of the signal that reaches the oscilloscope input[3].

Let's analyze the behavior of a passive probe. Figure C.4 shows the schematics of the equivalent circuit of a passive probe and of the input stage of an oscilloscope. The capacitance of the probe cable can be considered included in C_s

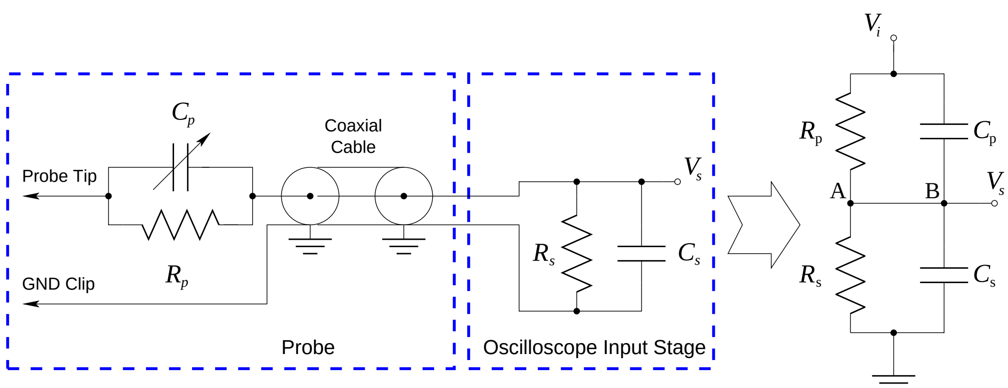


Figure C.4: Oscilloscope input stage and passive probe schematics.The equivalent circuit made of ideal components for the probe shielded cable is not shown.

Considering the voltage divider equation, we have

$$H(j\omega) = \frac{V_s}{V_i} = \frac{Z_s}{Z_p + Z_s}, \tag{C.1}$$

where

$$\frac{1}{Z_s} = j\omega C_s + \frac{1}{R_s}, \qquad \frac{1}{Z_p} = j\omega C_p + \frac{1}{R_p},$$

and then

$$Z_s = \frac{R_s}{j\omega\tau_s + 1}, \qquad Z_p = \frac{R_p}{j\omega\tau_p + 1}.$$

[3] Active probes can partially avoid this problems by amplifying the signal.

Defining the following parameters

$$\tau_p = C_p R_p, \quad \alpha = \frac{R_s}{R_s + R_p}, \qquad \beta = \frac{C_p}{C_s + C_p},$$

and after some tedious algebra, equation (C.1) becomes

$$H(j\omega) = \alpha \frac{1 + j\omega\tau_p}{1 + j\omega\frac{\alpha}{\beta}\tau_p},$$

which is the transfer function from the probe input to the oscilloscope input before the ideal amplification stage.

The DC and high frequency gain of the transfer function $H(j\omega)$ are respectively

$$H(0) = \alpha, \qquad H(\infty) = \beta.$$

The numerator and denominator of $H(j\omega)$ are respectively equal to zero, (the zeros and poles of H) when

$$\omega = \omega_z = j\frac{1}{\tau_p}, \qquad \omega = \omega_p = j\frac{\beta}{\alpha}\frac{1}{\tau_p}.$$

Figure C.5 shows the qualitative behavior of H for $\frac{\alpha}{\beta} > 1$.

C.3.1 Probe Frequency Compensation

By tuning the variable capacitor C_p of the probe, we can have three possible cases

$$\begin{aligned}
\frac{\alpha}{\beta} < 1 \quad &\Rightarrow \quad \text{over-compensation} \\
\frac{\alpha}{\beta} = 1 \quad &\Rightarrow \quad \text{compensation} \\
\frac{\alpha}{\beta} > 1 \quad &\Rightarrow \quad \text{under-compensation}
\end{aligned}$$

if $\alpha < \beta$ the transfer function attenuates more at frequencies above ω_z, and the input signal V_i is distorted.

if $\alpha = \beta$ the transfer function is constant and the input signal $V_i$will be undistorted, and attenuated by a factor α.

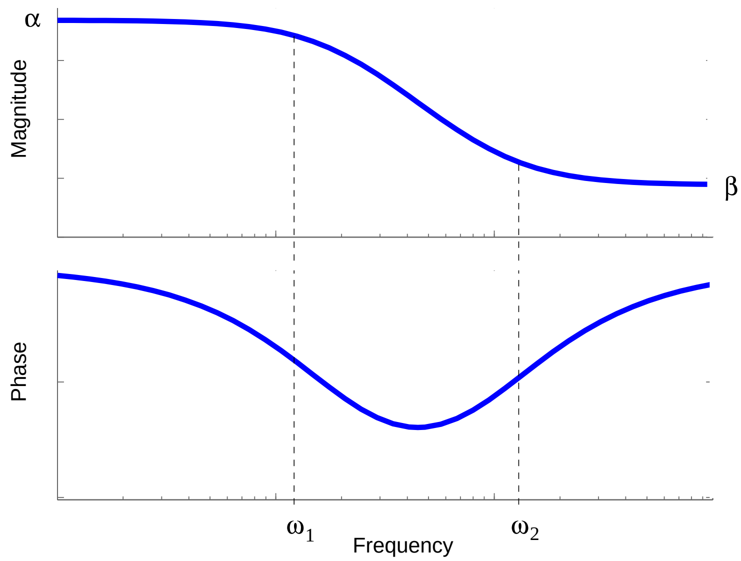


Figure C.5: Qualitative transfer function from the under compensated probe input to the oscilloscope input before the ideal amplification stage. As usual, the oscilloscope input is described having an impedance $R_s||C_s$.

if $\alpha > \beta$ the transfer function attenuates more at frequencies below ω_p and the input signal $V_i$is distorted.

The ideal case is indeed the compensated case, because we will have increased the input impedance by a factor α without distorting the signal.

The probe compensation can be tuned using a signal, which shows a clear distortion when it is filtered. A square wave signal is very useful in this case because, it shows a different distortion if the probe is under or over compensated. Figure C.6 sketches the expected square wave distortion for the two uncompensated cases.

It is worthwhile to notices that

$$\frac{\alpha}{\beta} = 1, \qquad \Rightarrow \frac{R_s}{R_p} = \frac{C_p}{C_s}.$$

This condition implies that:

- the voltage difference V_1 across R_s is equal the voltage difference V_2 across C_s, i.e $V_1 = V_2$

- the voltage difference V_3 across R_p is equal the voltage difference V_4 across C_p , i.e. $V_3 = V_4$
- and indeed $V_1 + V_2 = V_3 + V_4$.

This means that no current is flowing through the branch AB, and we can consider just the resistive branch of the circuit to calculate V_s. Applying the voltage divider equation, we finally get

$$V_s = \frac{R_s}{R_s + R} V_i$$

The capacitance of the oscilloscope does not affect the oscilloscope input anymore, and the oscilloscope+probe input impedance R_i becomes greater, i.e.

$$R_i = R_s + R_p.$$

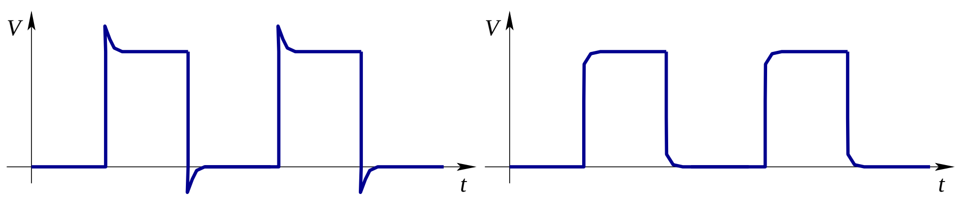


Figure C.6: Compensation of a passive probe using a square wave. Left figure shows an over compensated probe, where the low frequency content of the signal is attenuated. Right figure shows the under compensated case, where the high frequency content is attenuated.

C.4 Beam Trajectory

Let's consider the electron motion through one pair of plates.

The electron terminal velocity v_0 coming out from the gun can be easily calculated considering that its initial potential energy is entirely converted into kinetic energy, i.e

$$\frac{1}{2}\mu v_0^2 = eV_0, \quad \Rightarrow \quad v_0 = \sqrt{2\frac{eV_0}{\mu}},$$

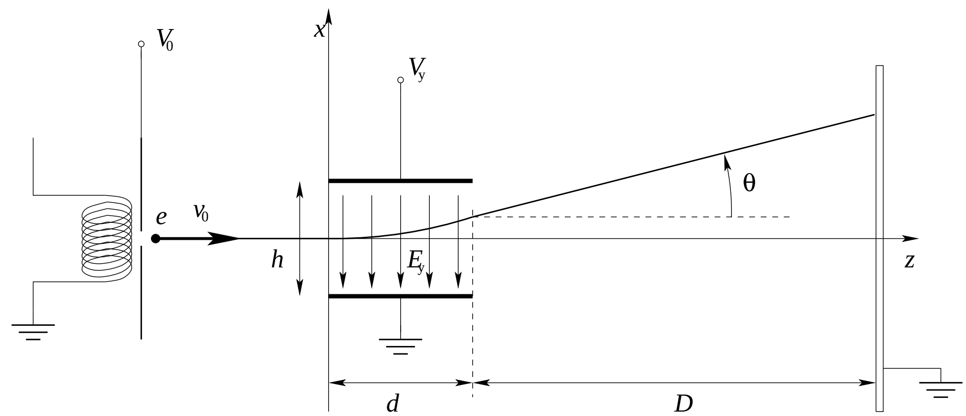


Figure C.7: CRT tube schematics. The electron enters into the electric field and makes a parabolic trajectory. After passing the electric field region it will have a vertical offset and deflection angle θ.

where μ is the electron mass, e the electron charge, and V_0 the voltage applied to the last anode.

If we apply a voltage V_y to the plates whose distance is h, the electrons will feel a force $F_y = eE_y$ due to an electric field

$$|E_y| = \frac{V_y}{h}.$$

The equation of dynamics of the electron inside the plates is

$$\begin{aligned} \mu\ddot{z} &= 0, \quad \Rightarrow \quad \dot{z} = v_0, \\ \mu\ddot{y} &= e|E_y|. \end{aligned}$$

Supposing that V_y is constant, the solution of the equation of motion will be

$$\begin{aligned} z(t) &= \sqrt{2\frac{eV_0}{\mu}}t, \\ y(t) &= \frac{1}{2}\frac{eV_y}{\mu h}t^2. \end{aligned}$$

Removing the dependency on the time t, we will obtain the electron beam trajectory , i.e.

$$y = \frac{1}{4h}\frac{V_y}{V_0}z^2,$$

which is a parabolic trajectory.

Considering that the electron is transversely accelerated until $z = d$, the total angular deflection θ will be

$$\tan\theta = \left(\frac{\partial y}{\partial z}\right)_{z=d} = \frac{1}{2}\frac{d}{h}\frac{V_y}{V_0}.$$

and displacement Y on the screen is

$$Y(V_y) = y(z = d) + \tan\theta D,$$

i.e.,

$$Y(V_y) = \frac{1}{2}\frac{d}{h}\frac{1}{V_0}(\frac{d}{2} + D)V_y.$$

Y is indeed proportional to the voltage applied to the plates through a rather complicated proportional factor.

The geometrical and electrical parameters of this proportional factor play a fundamental role in the resolution of the instrument. In fact, the smaller the distance h between the plates, or the smaller the gun voltage drop V_0, the larger is the displacement Y. Moreover, Y increases quadratically with the electron beam distance d.

C.4.1 CRT Frequency Limit

The electron transit time through the plates determine the maximum frequency that a CRT can plot. In fact, if the transit time τ is much smaller than the period T of the wave form $V(t)$, we have

$$V(t) \simeq \text{constant}, \qquad \text{if} \quad \tau \ll T,$$

and the signal is not distorted.

The transit time is

$$\tau = \frac{d}{v_0} = d\sqrt{\frac{\mu}{2eV_0}}.$$

Supposing that

$$\begin{cases} V_0 = 1\text{kV} \\ d = 20\text{mm} \\ \mu c^2 \simeq 0.5\text{MeV} \\ e = 1\text{eV} \end{cases} \quad \Rightarrow \quad \tau \simeq 1\text{ns}$$

Appendix D

Electromagnetic Field Noise

D.1 Introduction

Human and natural activities fill the surrounding space with electromagnetic fields (radiation) creating a very complex and unpredictable frequency spectrum of radiation. For example, domestic appliances, bulbs, fluorescent lights, and power line grids mainly irradiate at 60Hz and harmonics of 60Hz. Radios, televisions, wireless internet connections, and cellular phones networks are other typical sources, which fill the radiation spectrum from the kilohertz to the gigahertz region. Light mainly produced by the sun pervades the spectrum in the optical region. Radioactivity, gamma ray burst (GRB) emitted by astrophysical sources are for eaxmple responsible for filling the high and very high region of the spectrum.

Portion of this so complex spectrum can be attenuated by the so called electromagnetic shields but some others portions because of the energy involved cannot be effectively even attenuated.

The so called radio frequency noise can be easily attenuated (shielded) using a quite simple device known as the *Faraday cage*.

D.2 The Faraday Cage

Gauss's law states that a closed surface will prevent extern electrostatic fields from reaching the space enclosed by the surface. If the electric field is slowly varying i.e., its wavelength λ is large compared to the typical size d of the enclosure), then the field on the surface can be considered static and Gauss's law is then applicable. This enclosure is commonly called *Faraday cage*.

Using this crude approximation we can state that all frequencies much smaller than the following

$$\nu^* \sim \frac{c}{d}$$

where c is the speed of light, will be effectively attenuated. For example if $d = 1$ m then the Faraday cage will attenuate the external electromagnetic fields with frequencies much smaller than $\nu^* \sim 300$ MHz.

D.3 Practical Considerations

Normally, when we perform a measurement we cannot easily fit the lab in a small Faraday cage. Anyway, most of the time it is sufficient to enclose the physical system under measurement inside the cage . Then to perform the measurement we will have to connect the instrument sitting outside the cage to the system. The instruments leads acting like an antenna will still pick-up some of the ambient electromagnetic radiation. This effect can be amplified if we touch one of the leads increasing the antenna effect. A way to minimize this effect is to connect Faraday cages together. Reasonably good instruments have a built in Faraday cage connected to ground. Connecting the cages to ground will create a more or less single effective cage which will attenuate the electromagnetic noise pick-up.

Appendix E

Common Emitter BJT Amplifier

The common emitter BJT amplifier is one of the most simple design that allows to set the voltage amplification A_v quite independently from the BJT characteristics.

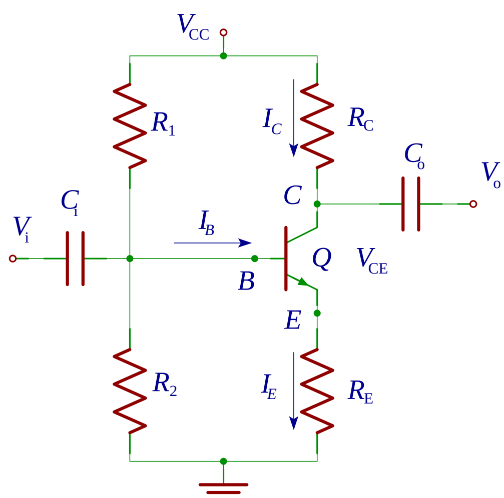


Figure E.1: BJT Common emitter amplifier with coupling capacitors C_i, and C_o.

To properly set the BJT working point, we have to forward bias the emitter base junction and reverse bias the collector base junction. But this is not enough if we want to build an amplifier. The other requirement is to set the voltage V_{CE} where the VCE characteristic is flat and wide enough

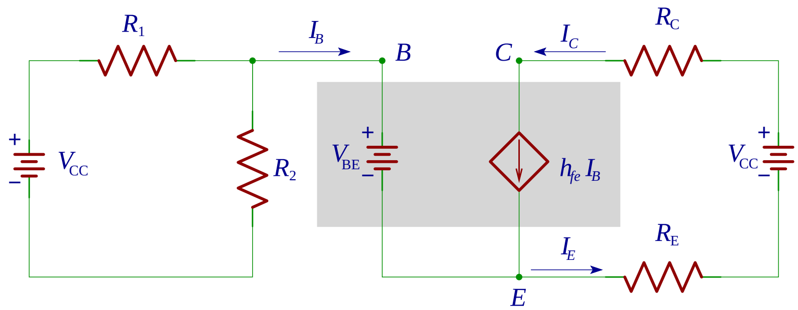


Figure E.2: Common emitter equivalent circuit which simplifies the BJT biasing understanding.

to accommodate the output signal excursion. In other words, we donŽt want the output to swing into the BJT saturation region or into the break down region.

The design parameters we have to set are are A_v, I_C,V_{CE},V_{CC}, and essentially, the VCE characteristics contains all the information we need to properly bias the BJT. As last remark, voltage gain and bias point are "intimately" related and cannot be completely independent.

E.1 Amplifier Design

The analysis of the circuit becomes quite easy if we observe from the V_{CE} characteristic that

$$I_C \gg I_B \,. \tag{E.1}$$

Applying KVL to the output mesh, we will have

$$V_{CC} = R_C I_C + R_E I_E + V_{CC} \simeq (R_C + R_E)\, I_C + V_{CE} \tag{E.2}$$

If we want to optimize the dynamic range of the amplifier, and neglecting the saturation region, we will have to set according to the $I_C - V_{CE}$ characteristic

$$V_{CE} \simeq \frac{1}{2} V_{CC}$$

Using this design condition and and voltage gain of this circuit we will have

$$R_E = \frac{1}{2\,(1 + A_v)} \frac{V_{CC}}{I_C}$$

This equations together with the gain equation (E.8) set the values R_C and R_E based only on the design parameters V_{CC},I_C, and A_v. Let's now find the values of the voltage divider which forward bias the base-emitter junction.

If I_B is negligible, then resistors R_1 and R_2 act as a simple voltage divider, i.e.

$$V_B \simeq \frac{R_2}{R_1 + R_2} V_{CC} \,. \tag{E.3}$$

and for KVL

$$V_B = V_{BE} + R_E I_E \simeq V_{BE} + R_E I_C$$

where V_{BE} must be the voltage drop of a forward polarized diode junction, typically between 6.0 V to 0.7 V.

Using the two expressions of V_B and after some algebra, we get

$$R_1 \simeq R_2 \left(\frac{V_{CC}}{V_{BE} + R_E I_C} - 1 \right) \,.$$

E.2 Resume

Summarizing the results we have for $I_C \gg I_B$

$$\alpha = \frac{1}{2\,(1 + A_v)} \tag{E.4}$$

$$R_E = \alpha \frac{V_{CC}}{I_C} \tag{E.5}$$

$$R_C = A_v R_E \tag{E.6}$$

$$R_1 = \left(\frac{V_{CC}}{V_{BE} + \alpha V_{CC}} - 1 \right) R_2 \tag{E.7}$$

E.3 Example

LetŽs set the following design values

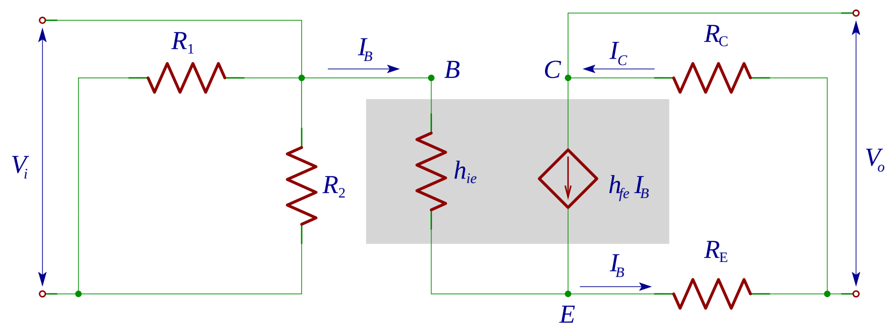


Figure E.3: Small signal circuit model for the common emitter BJT amplifier

$$\begin{cases} A_v & = & 10 \\ V_{CC} & = & 20\ \text{V} \\ I_C & = & 1\ \text{mA} \\ R_2 & = & 40\,\text{k}\Omega \end{cases} \quad\Rightarrow\quad \begin{cases} R_1 & \simeq & 452\,\text{k}\Omega \\ R_2 & \simeq & 40\,\text{k}\Omega \\ R_C & \simeq & 909\,\Omega \\ R_E & \simeq & 91\,\Omega \end{cases}$$

E.4 Amplifier Gain and Sign

Using the equivalent small signal circuit model for the BJT and considering the impedance of the ideal voltage and current sources we can construct the circuit show in figure E.3. Then from that figure it is finally easy to compute the voltage gain A_v and the input and output impedance R_i and R_o of the circuit.

In fact, considering that $I_B \ll I_C, R_E \gg h_{ie}$, the input and the output voltage are simply

$$\begin{cases} V_i & = & (h_{ie} + R_E)\, I_E \simeq R_E I_C \\ & & \\ V_o & = & R_C I_C \end{cases} , \Rightarrow A_v \simeq \frac{R_C}{R_E} \tag{E.8}$$

The Common Emitter BJT amplifier is an inverting stage. In fact, considering that

$$V_o = V_{CC} - (R_E + R_C)\, I_C \qquad \Rightarrow \qquad V_{CC} = V_o + (R_E + R_C)\, I_C$$

if I_C increases, then V_o must decrease to keep V_{CC} constant. If, we start with an input current and voltage in phase they will end up beeing out of phase by $180°$ degrees.

E.5 Input and Output Impedance

The input impedance is the impedance seen from the inputs lead , and can be easily computed considering that the ideal current source is an open circuit, i.e.

$$R_i = R_2 || R_1 || (h_{ie} + R_e)$$

The output impedance is then

$$R_o = R_C$$

E.6 I/O Coupling Capacitors

The coupling capacitors C_i will provide a way to send the input signal to amplifier without perturbing the DC bias of the BJT. Similarly, placing the capacitor C_o to the output will allow to connect a load without perturbing the DC bias of the BJT circuit.

Coupling capacitance should be selected to minimize the filtering effect on the amplifier response. For example, C_i will create a RC high pass filter with the R being the input resistance of the amplifier and C_o will do the same with the eventual resistance of the amplifier load.

E.7 Emitter Bypass Capacitor

Adding a so called bypass capacitor C_E in parallel with R_E will not change the DC bias of the BJT and will provide a the maximum possible voltage gain at high frequency as seen in the simple BJT amplifier circuit

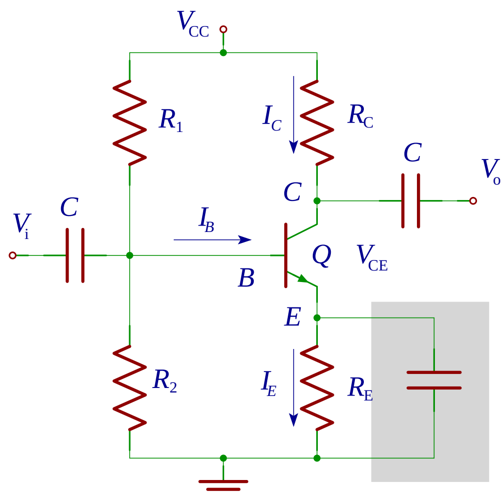


Figure E.4: BJT Common emitter amplifier with emitter bypassing capacitor.