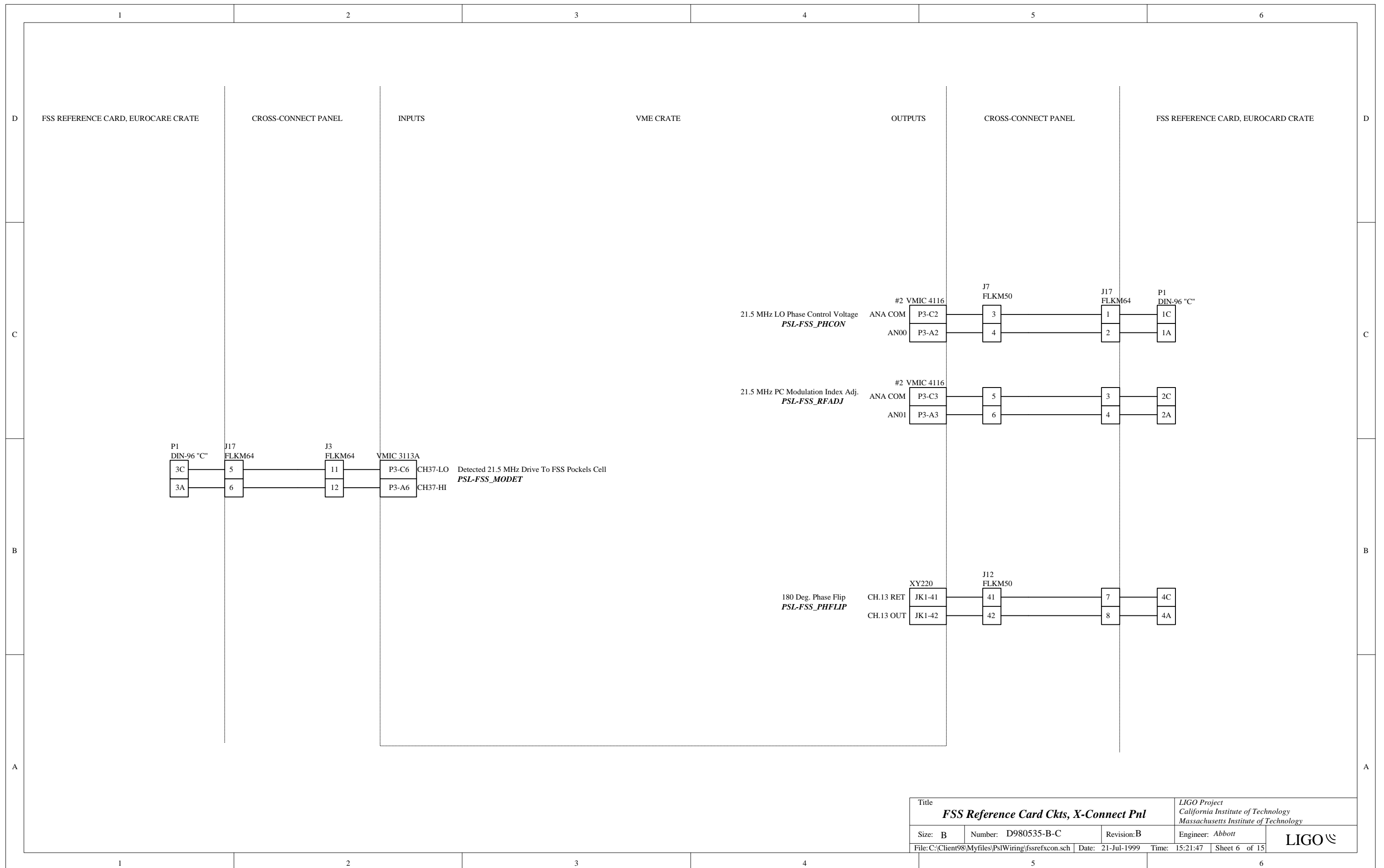
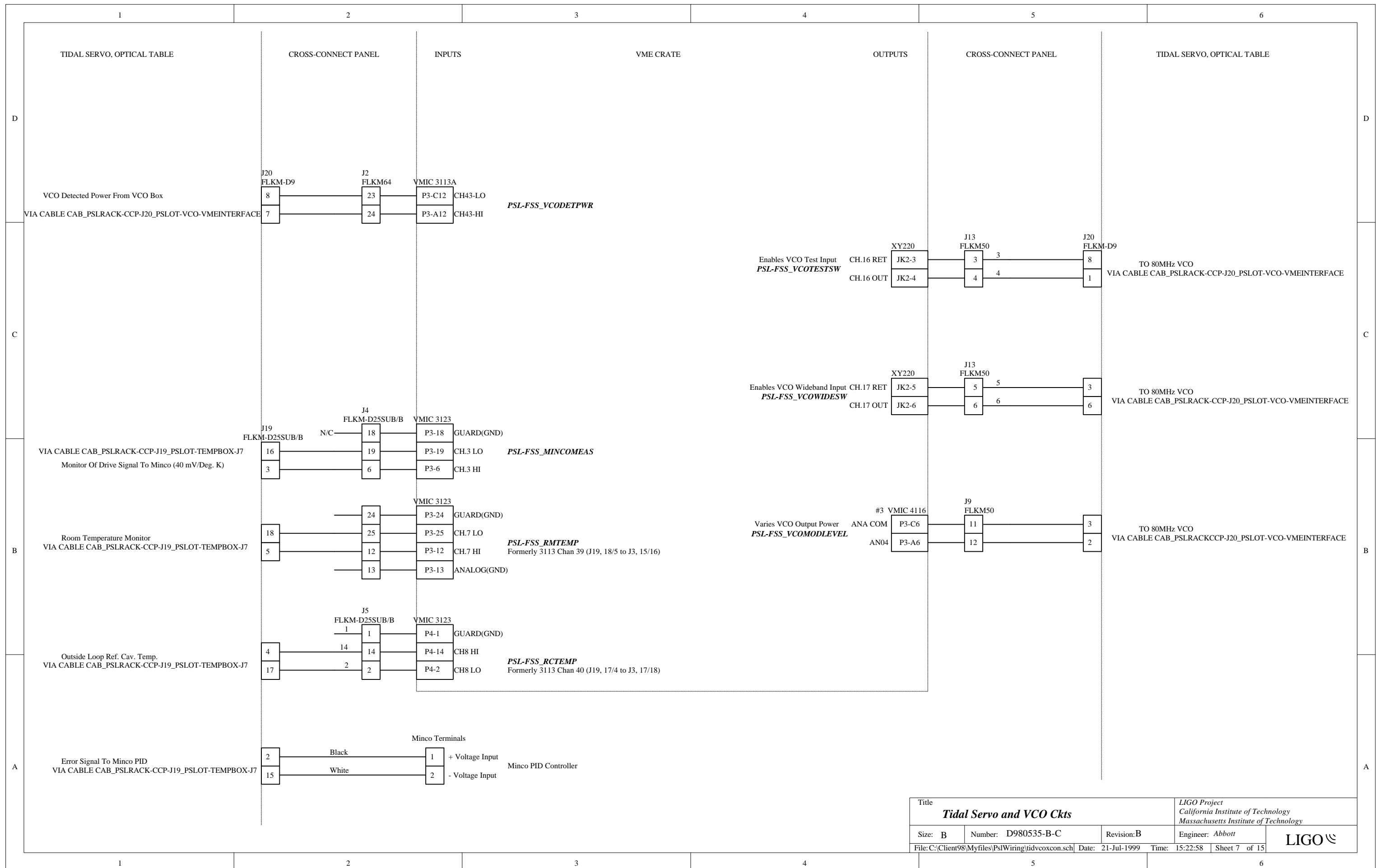


Title FSS Cross-Connects			LIGO Project California Institute of Technology Massachusetts Institute of Technology		
Size: B	Number: D980535-B-C	Revision: B	Engineer: Abbott	LIGO	
File: C:\Client98\Myfiles\PsiWiring\fsxcon.sch	Date: 21-Jul-1999	Time: 15:24:08	Sheet 5 of 15		

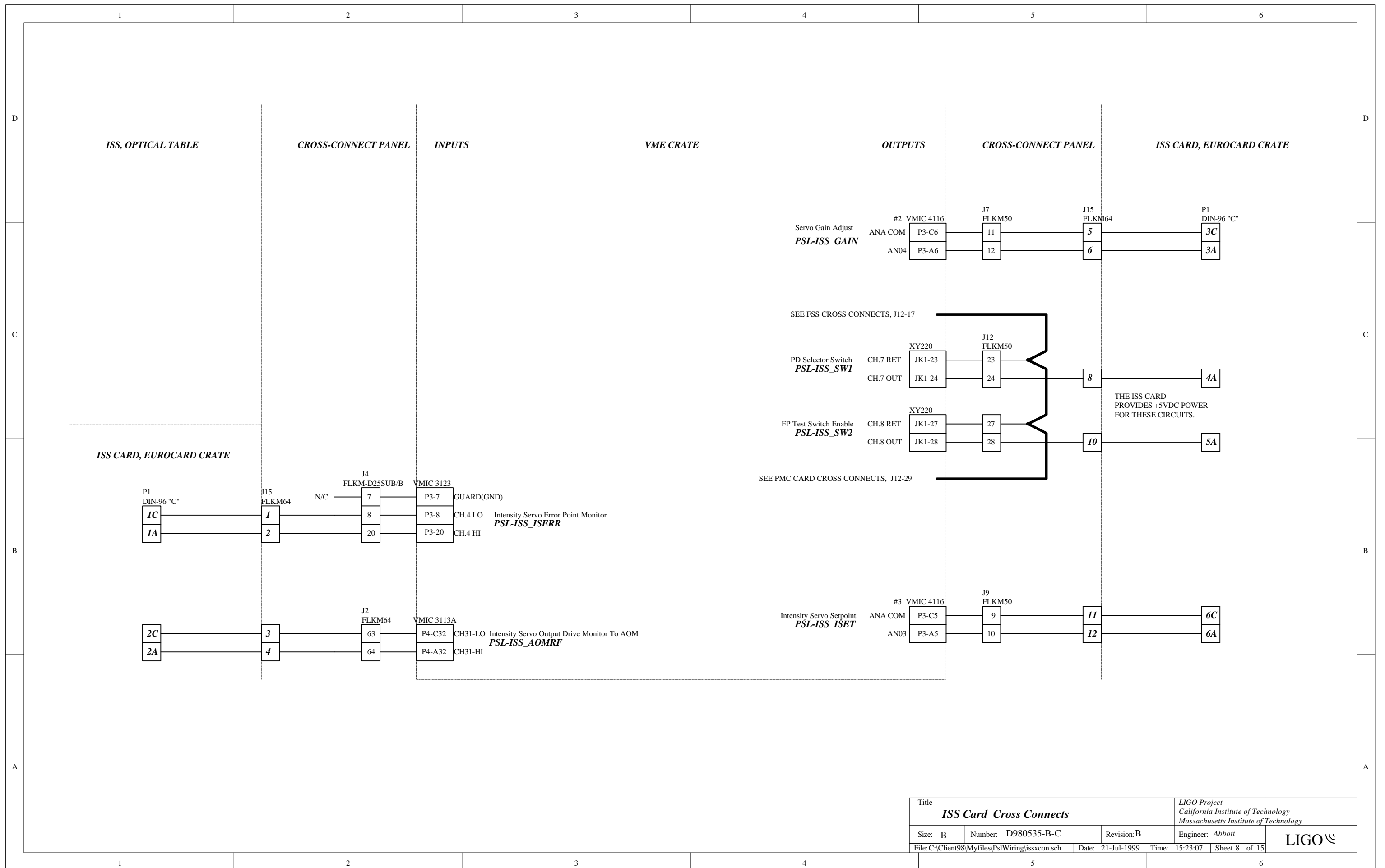


Title		LIGO Project	
FSS Reference Card Ckts, X-Connect Pnl		California Institute of Technology	
Massachusetts Institute of Technology		Engineer: Abbott	
Size: B	Number: D980535-B-C	Revision: B	File: C:\Client98\Myfiles\Psi\Wiring\fssexcon.sch
Date: 21-Jul-1999	Time: 15:21:47	Sheet 6	of 15

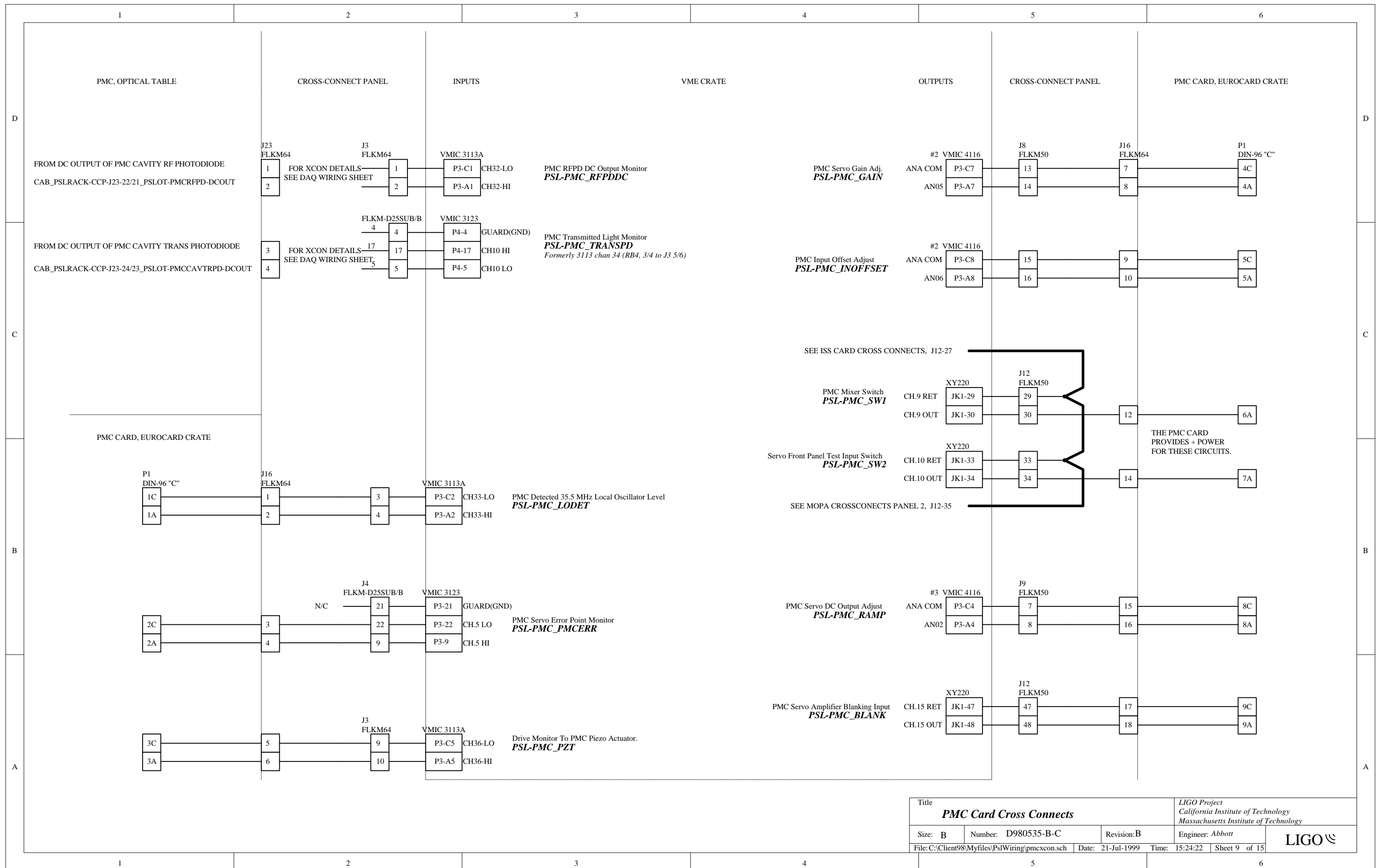




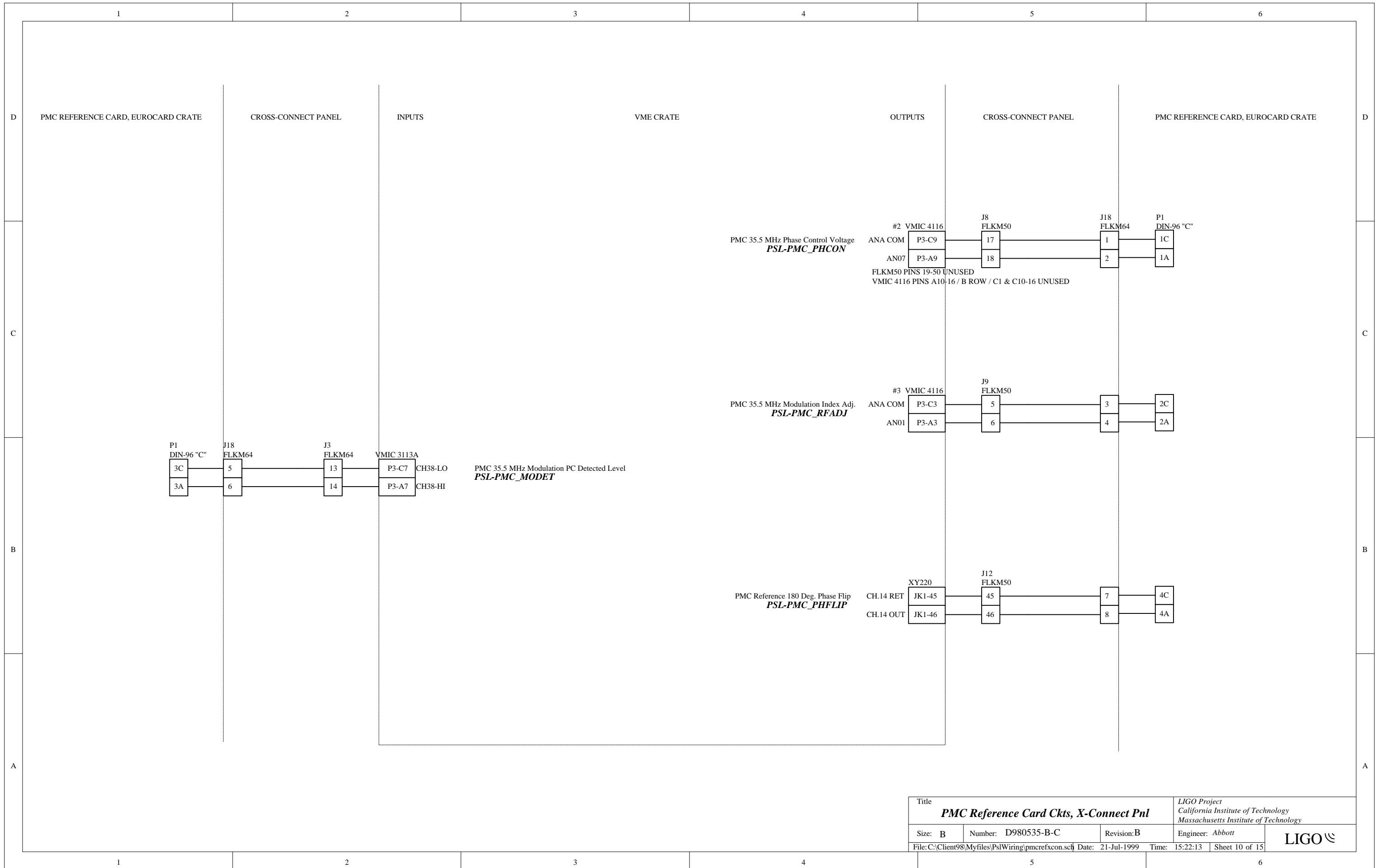
Title		LIGO Project	
Tidal Servo and VCO Ckts		California Institute of Technology	
Size: B		Massachusetts Institute of Technology	
Number: D980535-B-C	Revision: B	Engineer: Abbott	LIGO
File: C:\Client98\Myfiles\PsiWiring\tidvcocon.sch	Date: 21-Jul-1999	Time: 15:22:58	
Sheet 7 of 15			



Title ISS Card Cross Connects			LIGO Project California Institute of Technology Massachusetts Institute of Technology		
Size: B	Number: D980535-B-C	Revision: B	Engineer: Abbott	LIGO	
File: C:\Client98\Myfiles\PsiWiring\issxcon.sch	Date: 21-Jul-1999	Time: 15:23:07	Sheet 8 of 15		

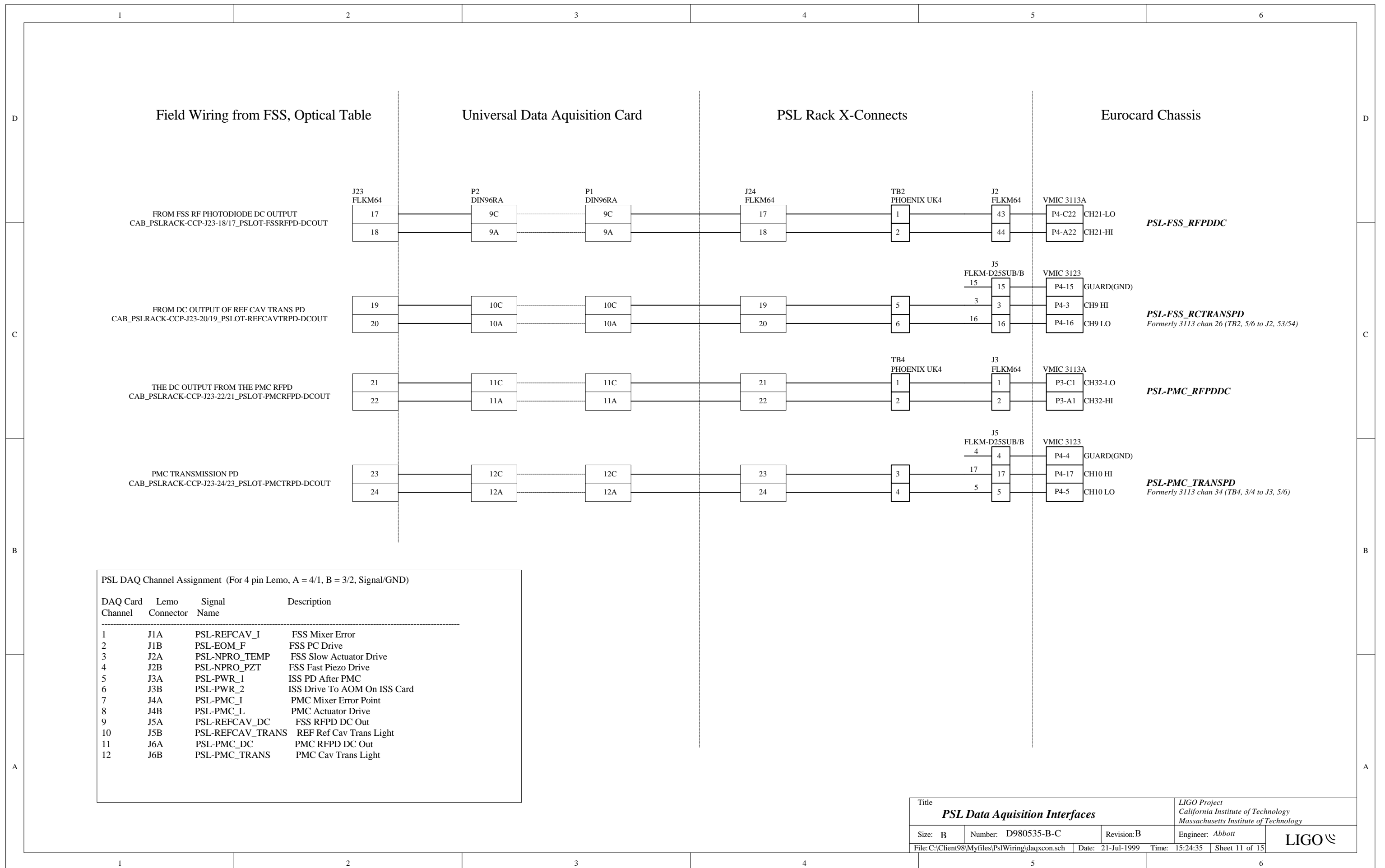


Title PMC Card Cross Connects			LIGO Project California Institute of Technology Massachusetts Institute of Technology		
Size: B	Number: D980535-B-C	Revision: B	Engineer: Abbott	LIGO	
File: C:\Client98\Myfiles\PsiWiring\pmcxcon.sch	Date: 21-Jul-1999	Time: 15:24:22	Sheet 9 of 15		

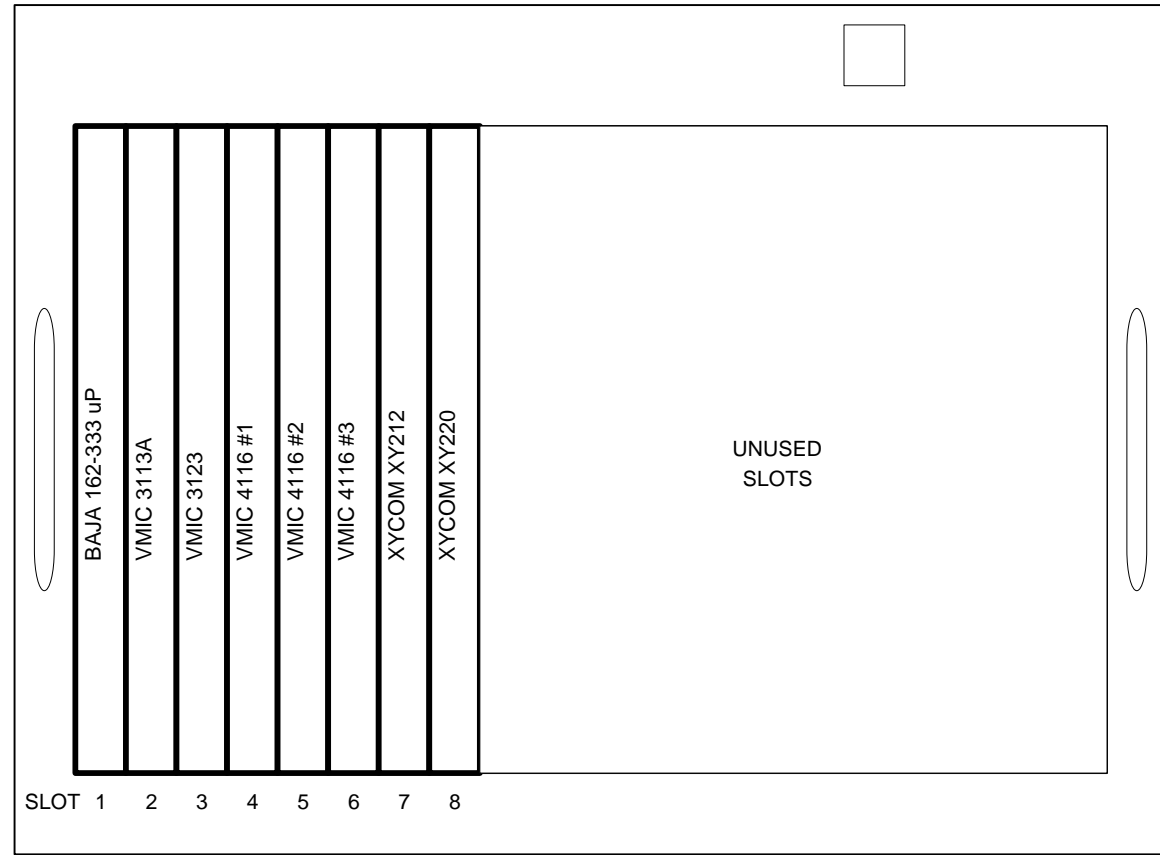


Title		LIGO Project	
PMC Reference Card Ckts, X-Connect Pnl		California Institute of Technology	
Massachusetts Institute of Technology		Engineer: <i>Abbott</i>	
Size: B	Number: D980535-B-C	Revision: B	
File: C:\Client98\Myfiles\Psi\Wiring\pmcrefxcon.sch	Date: 21-Jul-1999	Time: 15:22:13	Sheet 10 of 15

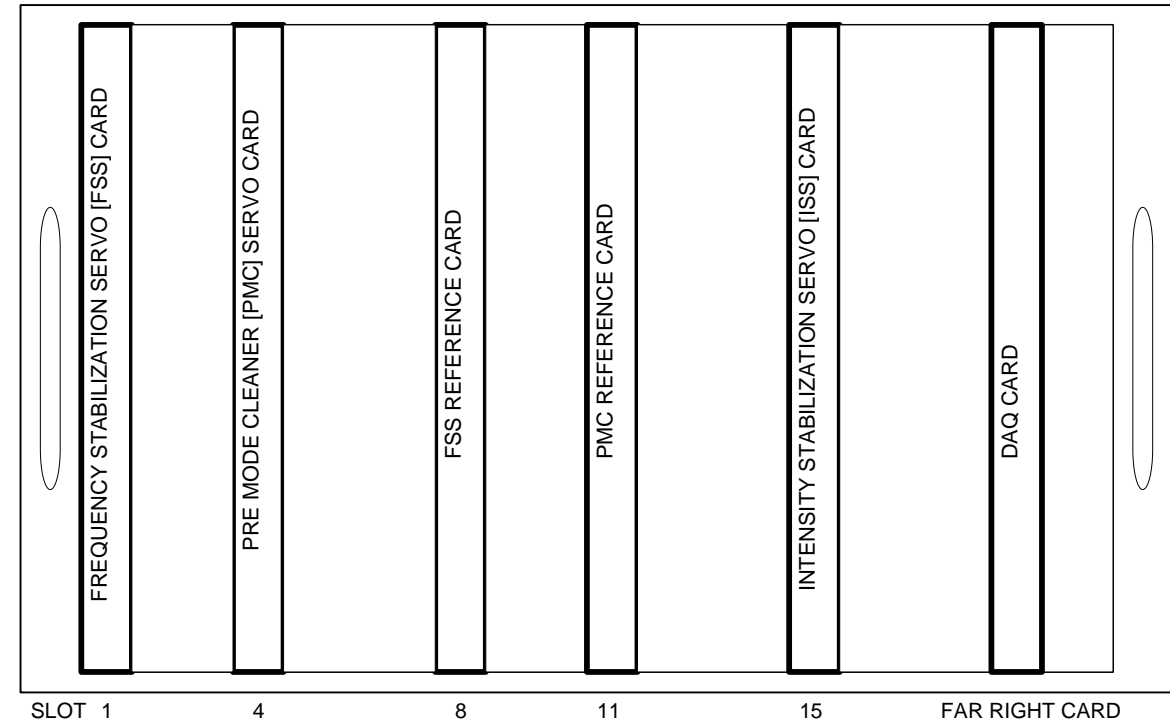





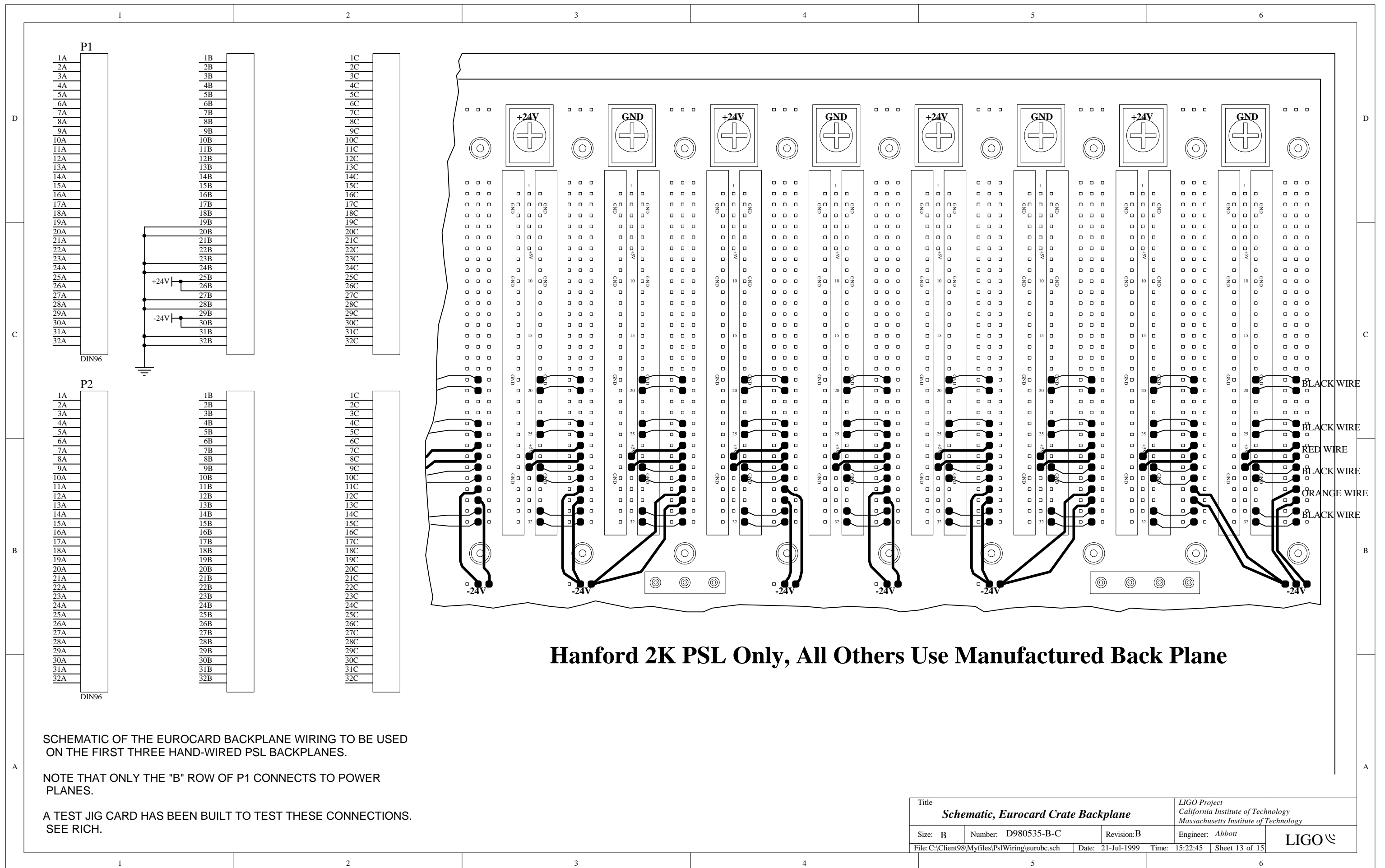
VME CRATE
CARD POSITIONS



EUROCARD CRATE
CARD POSITIONS



Title Assembly Drawing, PSL Electronics Rack			LIGO Project California Institute of Technology Massachusetts Institute of Technology		
Size: B	Number: D980535-B-C	Revision: B	Engineer: Abbott	LIGO 	
File: C:\Client98\Myfiles\Psi\Wiring\cratefp.sch	Date: 21-Jul-1999	Time: 15:23:23	Sheet 12 of 15		



Hanford 2K PSL Only, All Others Use Manufactured Back Plane

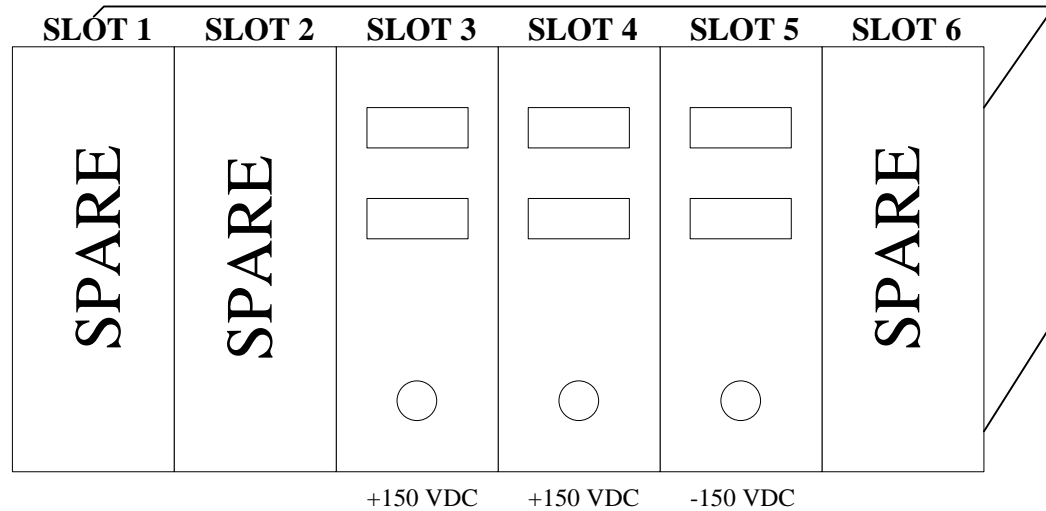
SCHEMATIC OF THE EUROCARD BACKPLANE WIRING TO BE USED ON THE FIRST THREE HAND-WIRED PSL BACKPLANES.

NOTE THAT ONLY THE "B" ROW OF P1 CONNECTS TO POWER PLANES.

A TEST JIG CARD HAS BEEN BUILT TO TEST THESE CONNECTIONS. SEE RICH.

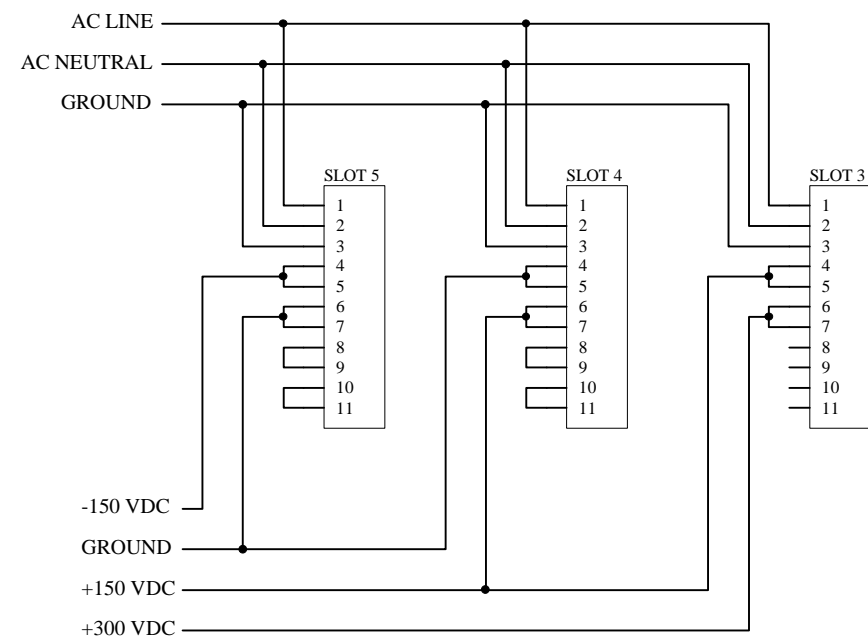
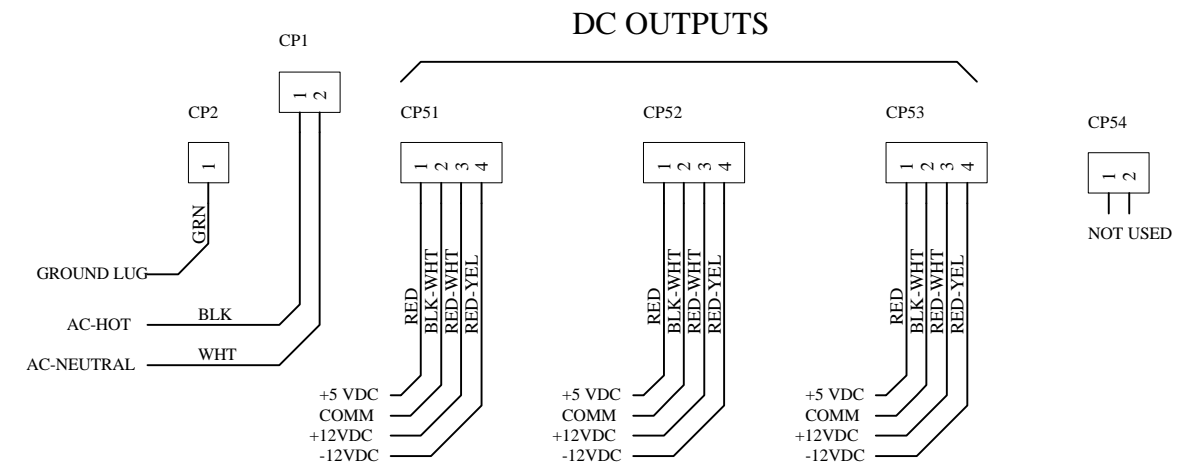
Title		LIGO Project	
Schematic, Eurocard Crate Backplane		California Institute of Technology Massachusetts Institute of Technology	
Size: B	Number: D980535-B-C	Revision: B	Engineer: Abbott
File: C:\Client98\Myfiles\PslWiring\eurobc.sch	Date: 21-Jul-1999	Time: 15:22:45	Sheet 13 of 15





Supplies used are Kepco PCX200-0.1MAT, 200 VDC Variable Supply.
 Slot 3 and 4 are in series aiding for the PMC.
 Slot 4 and 5 are for the Pockels Cell driver +/- 150 VDC for the FSS.

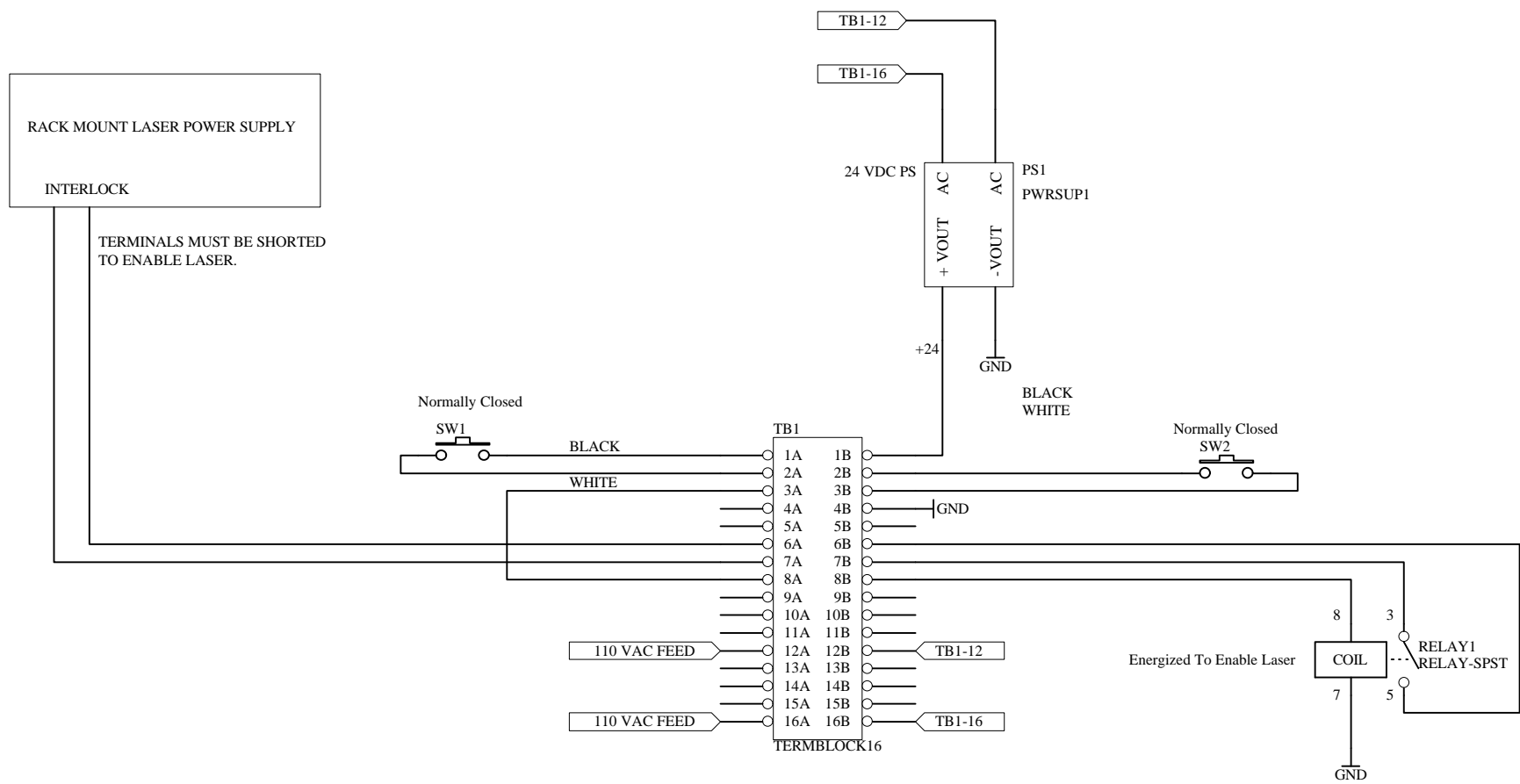
+/- 15VDC SUPPLIES FOR FSS AND PMC RFPDS



Title Kepco Power Supply Details		LIGO Project California Institute of Technology Massachusetts Institute of Technology	
Size: B	Number: D980535-B-C	Revision: B	Engineer: Abbott
File: C:\Client98\Myfiles\PsiWiring\kepco_ps.sch	Date: 21-Jul-1999	Time: 15:22:29	Sheet 14 of 15



LIVINGSTON PSL ONLY



Title Laser Safety Interlock Details (LLO)			LIGO Project California Institute of Technology Massachusetts Institute of Technology	
Size: B	Number: D980535-B-C	Revision: B	Engineer: Abbott	LIGO
File: C:\Client98\Myfiles\PsiWiring\safetysys.Sch	Date: 21-Jul-1999	Time: 15:23:39	Sheet 15 of 15	