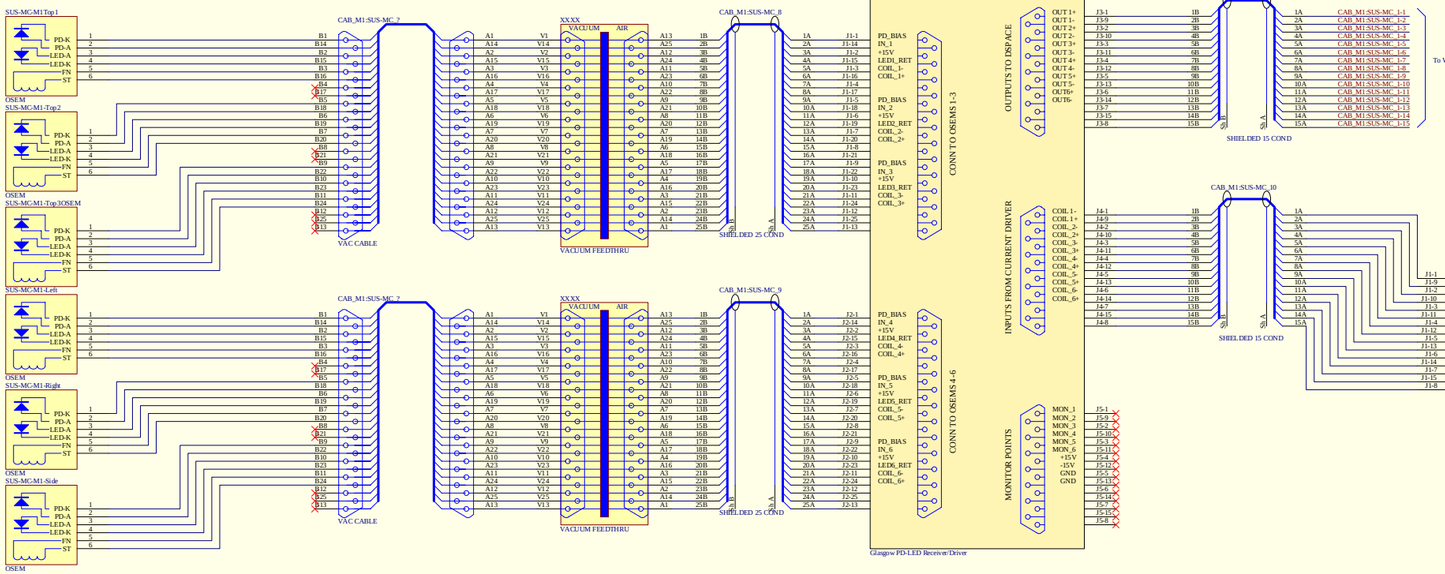
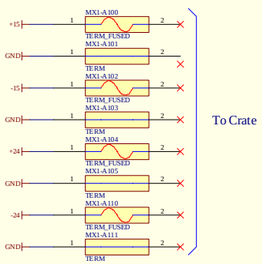
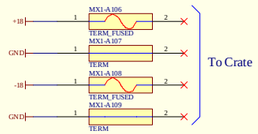
M1



LIGO EuroCard Crate Power Connections



Glasgow EuroCard Crate Power Connections

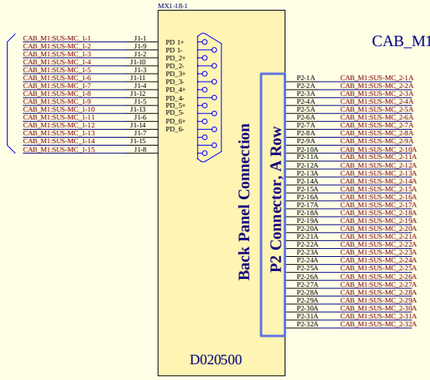


Title: **LASTI Adv LIGO MC Test Stand Wiring**			LIGO Laboratory, California Institute of Technology, Massachusetts Institute of Technology
Size: D	DCC Number: D040178	SCH / PCB Revision: 0	Engineer: P.Fritschel
File: E:\hcaGligo\users\jay\AdvLIGO_SUS_LASTI\DSpaceControls\AdvLIGO_MCControls1.SchDoc			Date: 5/27/2004 Time: 11:43:36 AM Sheet 1 of 3

CAB_M1:SUS-MC_2 is a daisy-chained cable on the P2

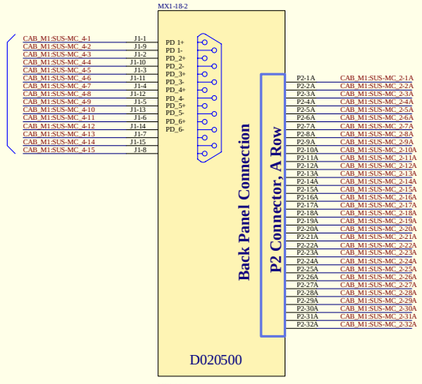
M1 PD Signals
From Glascow PD-LED, Sheet 1

6 channels (1-6 of ADC)



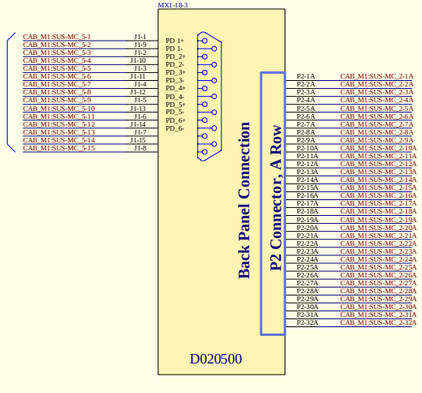
M2 PD Signals
From Sat Amp, Sheet 3

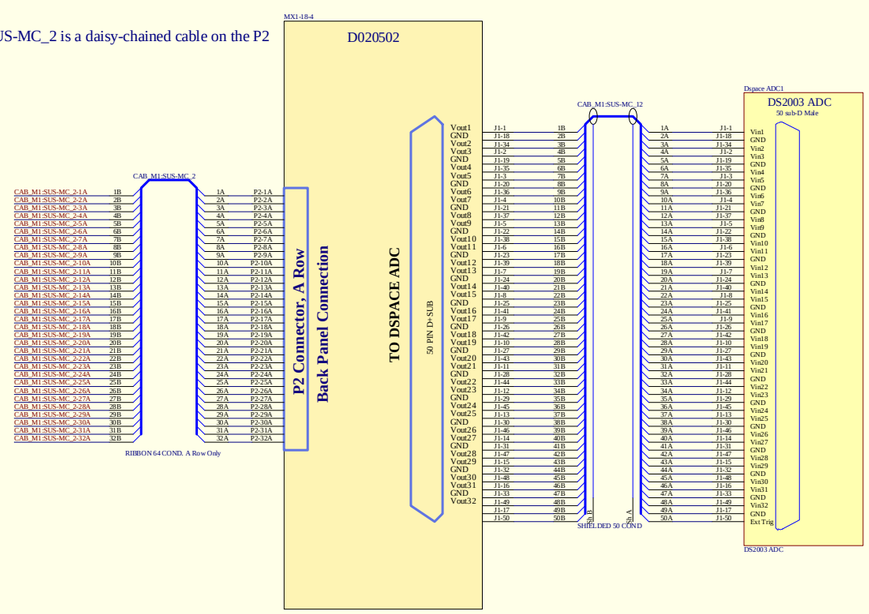
4 channels (7-10 of ADC)



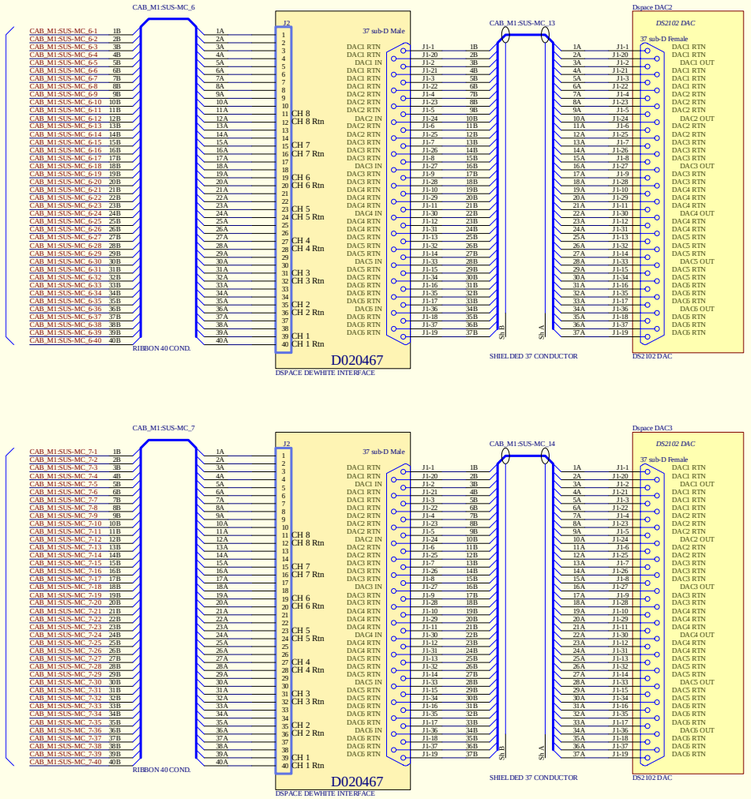
M3 PD Signals
From Sat Amp, Sheet 3

4 channels (11-14 of ADC)

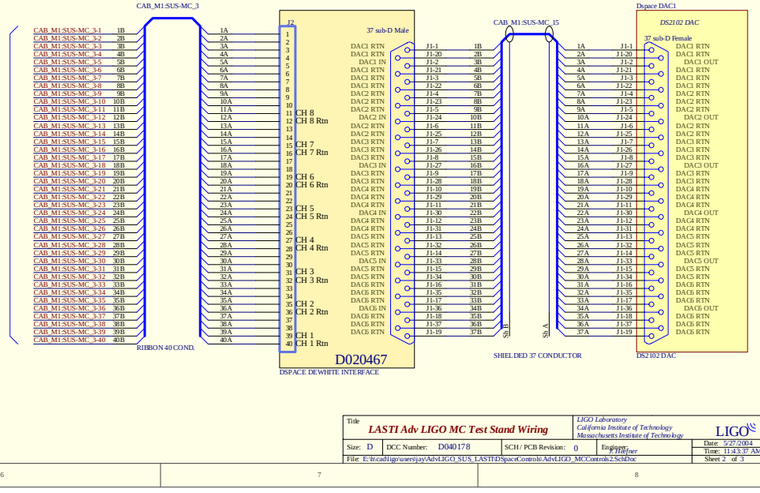




M2/M3 Anti-Image Board, Sheet 3



M1 Anti-Image Board, Sheet 1



Title: LASTI Adv LIGO MC Test Stand Wiring	LIGO Laboratory, California Institute of Technology, Massachusetts Institute of Technology			LIGO
Size: D	DCC Number: D040178	SCH / PCB Revision: 0	Engineer: Peter Fritschel	Date: 5/27/2004
File: E:\ics\edit\gjc\users\jay\AdvLIGO_SUS_LASTI\DSpaceControls\AdvLIGO_MCControls2.SchDoc				Time: 11:43:37 AM
				Sheet 2 of 3

M2

SUS-MC-M2-LL
SUS-MC-M2-UR
SUS-MC-M2-UL
SUS-MC-M2-LR
OSEM
PD-K
PD-A
LED-A
LED-K
FN
ST
VAC CABLE
CAB_M1:SUS-MC_7
XXXX
VACUUM
AIR
VACUUM FEEDTHRU
CAB_M1:SUS-MC_16
SHIELDED 25 COND
MX1-SMA
SATELLITE MODULE
D961289 Rev. 01
LL PD K
LL PD A
LL LED A
LL LED K
LL Coil RTN
LL Coil Out
UR PD K
UR PD A
UR LED A
UR LED K
UR Coil RTN
UR Coil Out
LR PD K
LR PD A
LR LED A
LR LED K
LR Coil RTN
LR Coil Out
S PD K
S PD A
S LED A
S LED K
S Coil Out
S Coil RTN
UL Mon
LL Mon
UR Mon
LR Mon
S Mon
GND
NC
Chassis
CAB_M1:SUS-MC_19
Satellite Amp Adapter
D010069
DB15 Female
DB15 Male
UL Out
LL Out
UR Out
LR Out
Side Out
UL Rtn
LL Coil
LL Rtn
UR Coil
UR Rtn
LR Rtn
LR Coil
Side Rtn
S Coil
+24V
GND
-24V
To White/AA TBD, Sheet 2
CAB_M1:SUS-MC_4
CAB_M1:SUS-MC_21
SHIELDED 15 COND
MX1-18-12A
SOS Coil Driver
D010001, Rev A
SOS COIL DRIVER
MX1-A112
MX1-A113
MX1-A120
MX1-A121
TERM_FUSED
TERM
+24
GND
-24
MX1-18-11B
OUTPUTS
INPUTS
LSC 8 Ch. Anti-Image Board
D000186 Rev. A, B
FP LEMOS
M2 UL
M2 LL
M2 UR
M2 LR
M3 UL
M3 LL
LSC ANTI-IMAGE(MIRROR)
CAB_M1:SUS-MC_23
CAB_M1:SUS-MC_24
CAB_M1:SUS-MC_25
CAB_M1:SUS-MC_26
CAB_M1:SUS-MC_27
CAB_M1:SUS-MC_28
COAX
UL In
LL In
UR In
LR In
Side In
GND
CAB_M1:SUS-MC_6-1
CAB_M1:SUS-MC_6-2
CAB_M1:SUS-MC_6-3
CAB_M1:SUS-MC_6-4
CAB_M1:SUS-MC_6-5
CAB_M1:SUS-MC_6-6
CAB_M1:SUS-MC_6-7
CAB_M1:SUS-MC_6-8
CAB_M1:SUS-MC_6-9
CAB_M1:SUS-MC_6-10
CAB_M1:SUS-MC_6-11
CAB_M1:SUS-MC_6-12
CAB_M1:SUS-MC_6-13
CAB_M1:SUS-MC_6-14
CAB_M1:SUS-MC_6-15
CAB_M1:SUS-MC_6-16
CAB_M1:SUS-MC_6-17
CAB_M1:SUS-MC_6-18
CAB_M1:SUS-MC_6-19
CAB_M1:SUS-MC_6-20
CAB_M1:SUS-MC_6-21
CAB_M1:SUS-MC_6-22
CAB_M1:SUS-MC_6-23
CAB_M1:SUS-MC_6-24
CAB_M1:SUS-MC_6-25
CAB_M1:SUS-MC_6-26
CAB_M1:SUS-MC_6-27
CAB_M1:SUS-MC_6-28
CAB_M1:SUS-MC_6-29
CAB_M1:SUS-MC_6-30
CLK + IN
CLK - IN
J6-CASE
CASE
Dspace Dewhite Interface, Sheet 2

M3

SUS-MC-M3-LL
SUS-MC-M3-UR
SUS-MC-M3-UL
SUS-MC-M3-LR
CAB_M1:SUS-MC_2
CAB_M1:SUS-MC_17
CAB_M1:SUS-MC_18
CAB_M1:SUS-MC_20
MX1-SMB
CAB_M1:SUS-MC_1
CAB_M1:SUS-MC_22
CAB_M1:SUS-MC_29
CAB_M1:SUS-MC_30
MX1-A114
MX1-A115
MX1-A122
MX1-A123
MX1-18-13B
LSC ANTI-IMAGE(MIRROR)
CAB_M1:SUS-MC_7-1
CAB_M1:SUS-MC_7-2
CAB_M1:SUS-MC_7-3
CAB_M1:SUS-MC_7-4
CAB_M1:SUS-MC_7-5
CAB_M1:SUS-MC_7-6
CAB_M1:SUS-MC_7-7
CAB_M1:SUS-MC_7-8
CAB_M1:SUS-MC_7-9
CAB_M1:SUS-MC_7-10
CAB_M1:SUS-MC_7-11
CAB_M1:SUS-MC_7-12
CAB_M1:SUS-MC_7-13
CAB_M1:SUS-MC_7-14
CAB_M1:SUS-MC_7-15
CAB_M1:SUS-MC_7-16
CAB_M1:SUS-MC_7-17
CAB_M1:SUS-MC_7-18
CAB_M1:SUS-MC_7-19
CAB_M1:SUS-MC_7-20
CAB_M1:SUS-MC_7-21
CAB_M1:SUS-MC_7-22
CAB_M1:SUS-MC_7-23
CAB_M1:SUS-MC_7-24
CAB_M1:SUS-MC_7-25
CAB_M1:SUS-MC_7-26
CAB_M1:SUS-MC_7-27
CAB_M1:SUS-MC_7-28
CAB_M1:SUS-MC_7-29
CAB_M1:SUS-MC_7-30
Dspace Dewhite Interface, Sheet 2

Title	LASTI Adv LIGO MC Test Stand Wiring
	LIGO Laboratory, California Institute of Technology, Massachusetts Institute of Technology
Size: D	DCC Number: D040178
SCH / PCB Revision: 0	Engineer: P.Fritschel
Date: 5/27/2004	Time: 11:43:37 AM
File: E:\ligo\users\jay\AdvLIGO_SUS_LASTI\SpaceControls\AdvLIGO_MCControls.1.SchDoc	Sheet 3 of 3