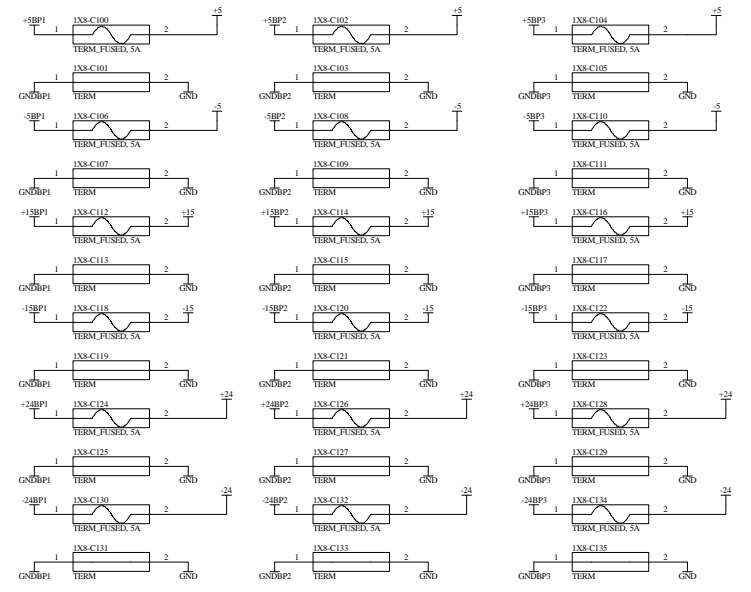
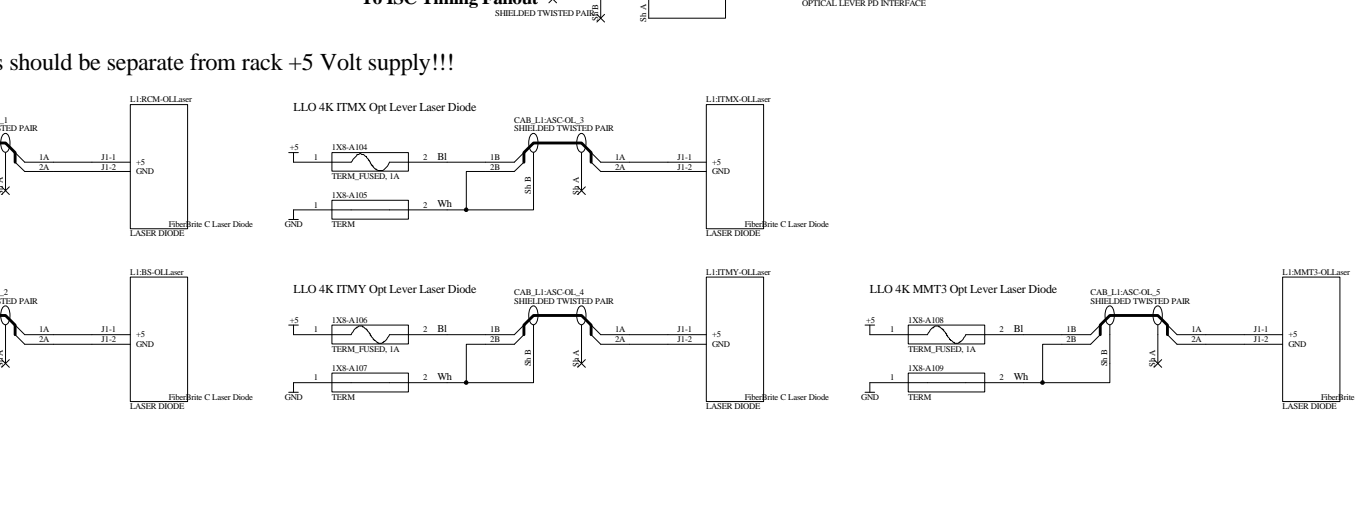
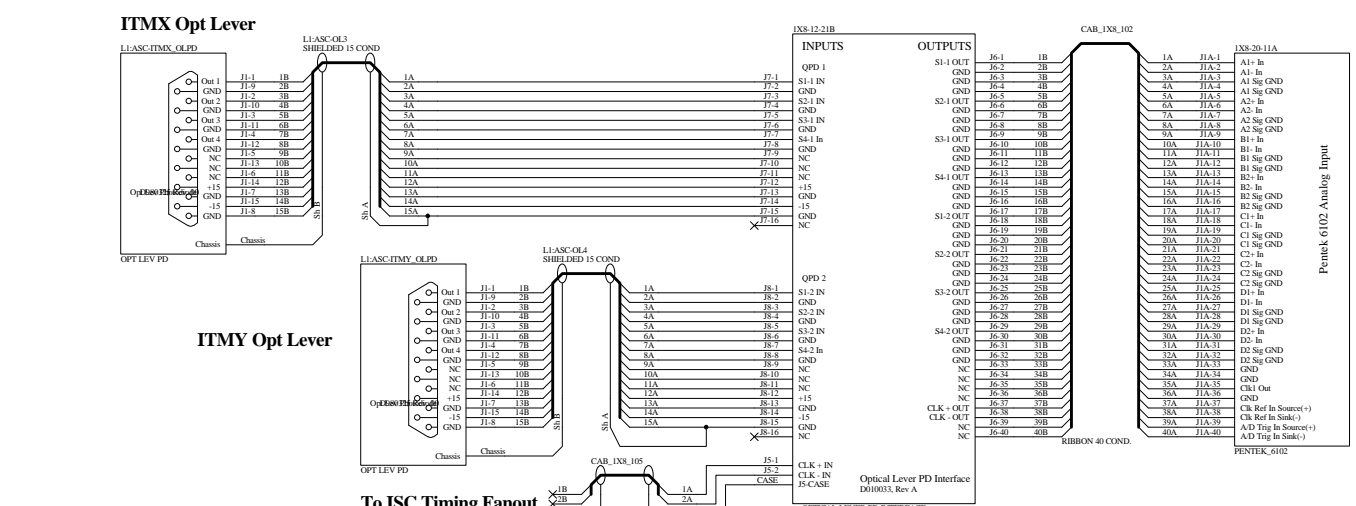
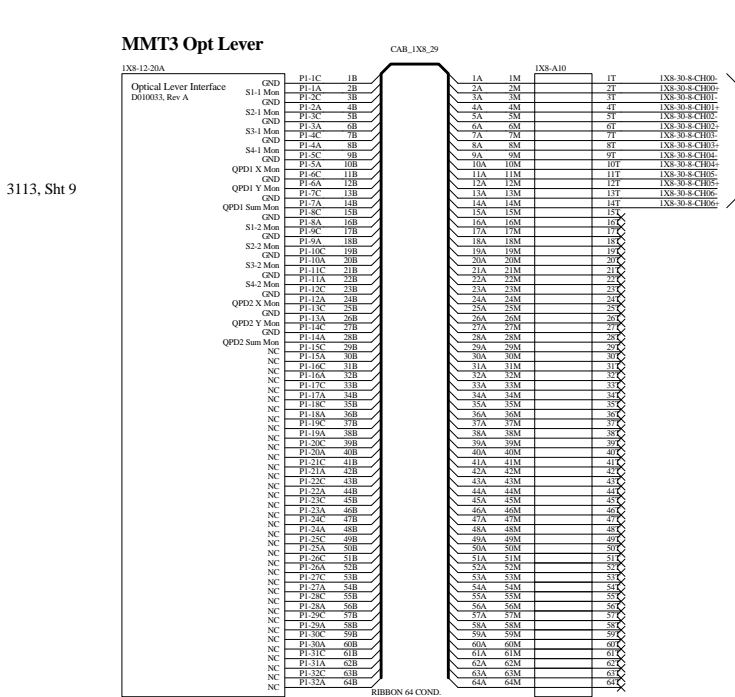
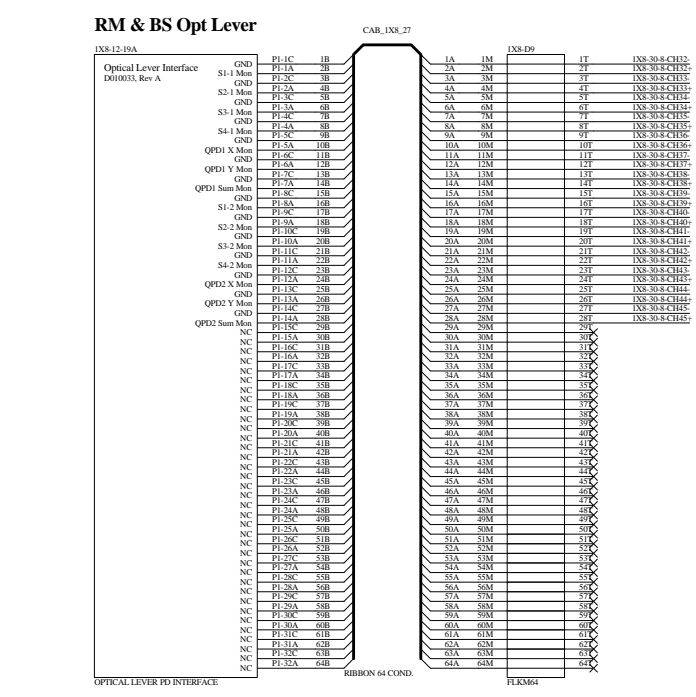
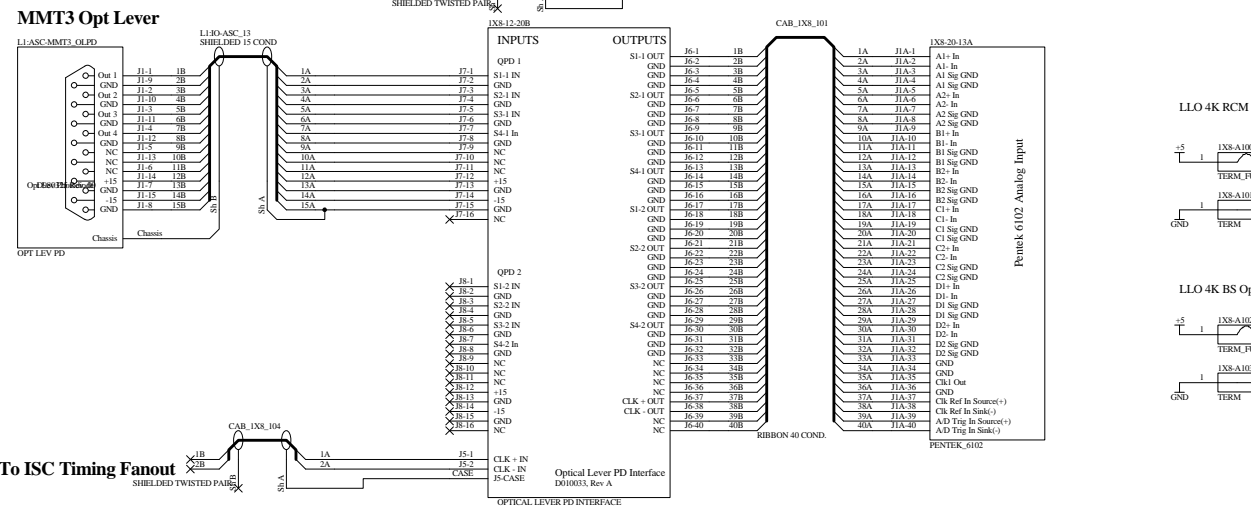
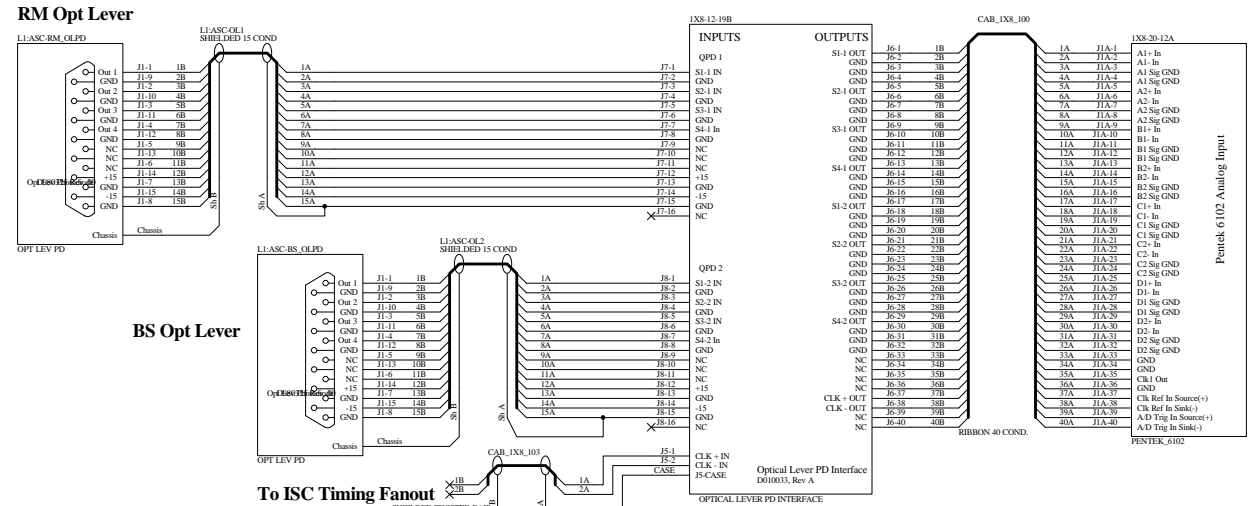
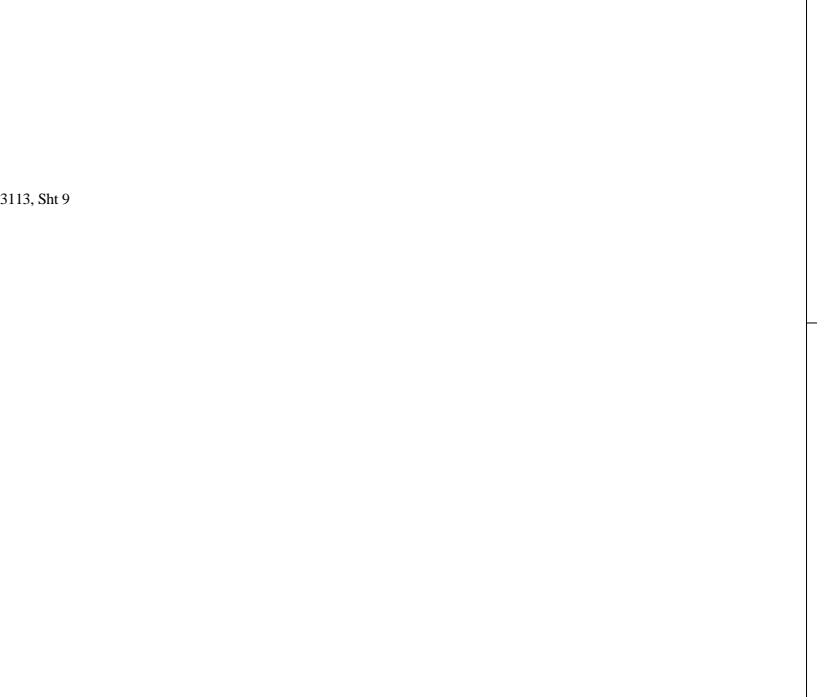
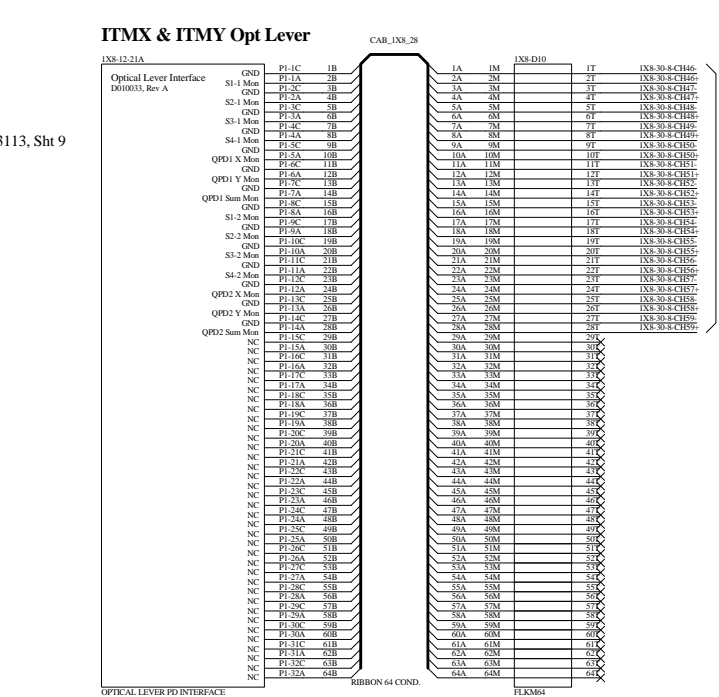


Note:
 Power Connections for Eurocard modules are made through the backplane and are designated as XXBP1. Bulk power supply connections to the eurocard backplane lugs are designated +XX.

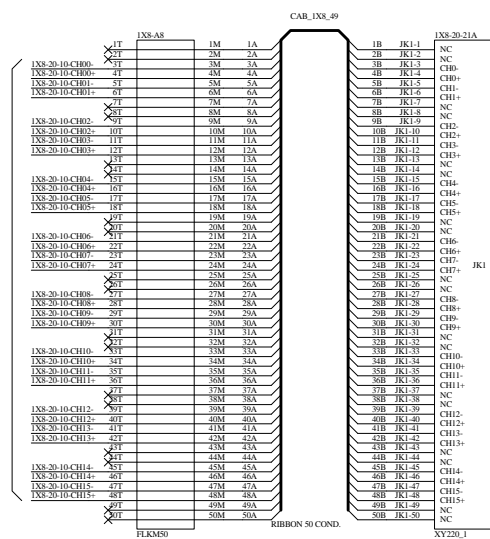




Note: Laser +5 Volts should be separate from rack +5 Volt supply!!!

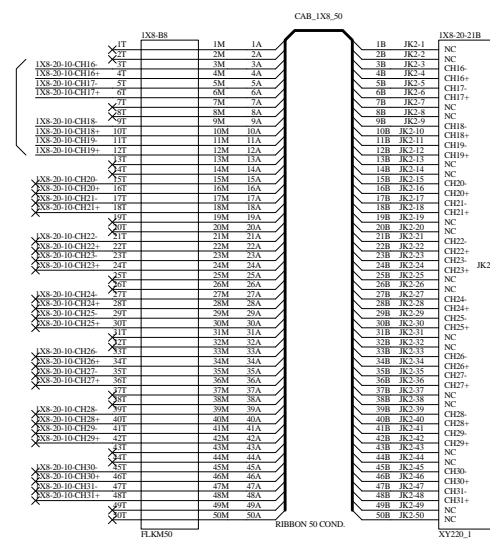


FE Crate 1X8-20



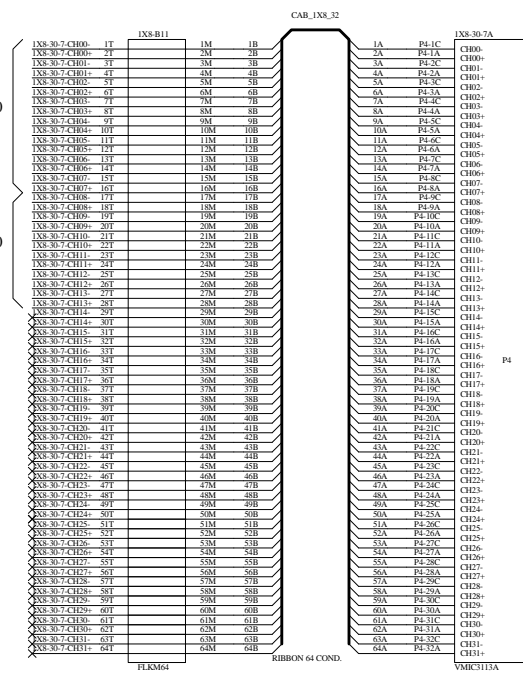
XY220 Card 3

Dewhite 2 Sh. 10



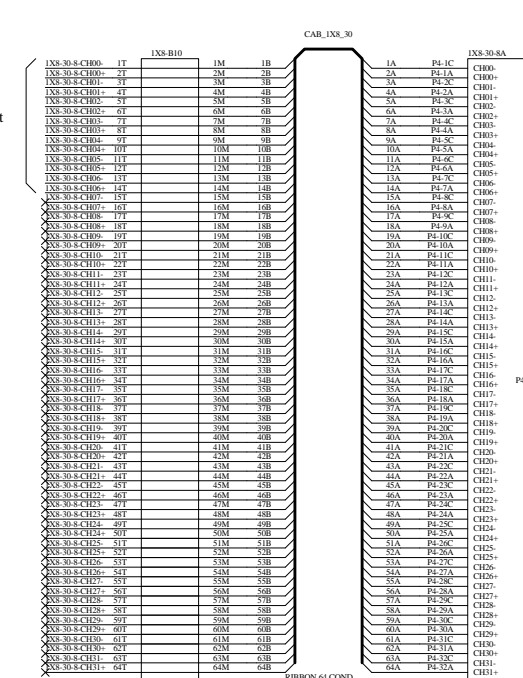
XY220 Card 3

Aux Crate 1X8-30



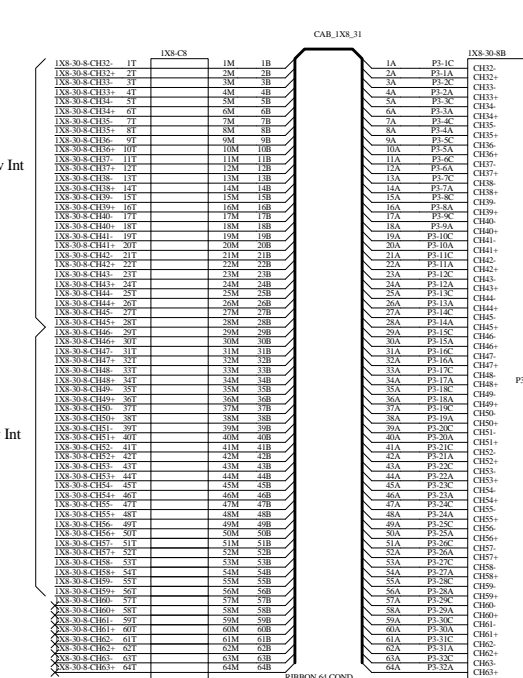
3113 Card 2

MMT 3 Opt Lev Int Sheet 8



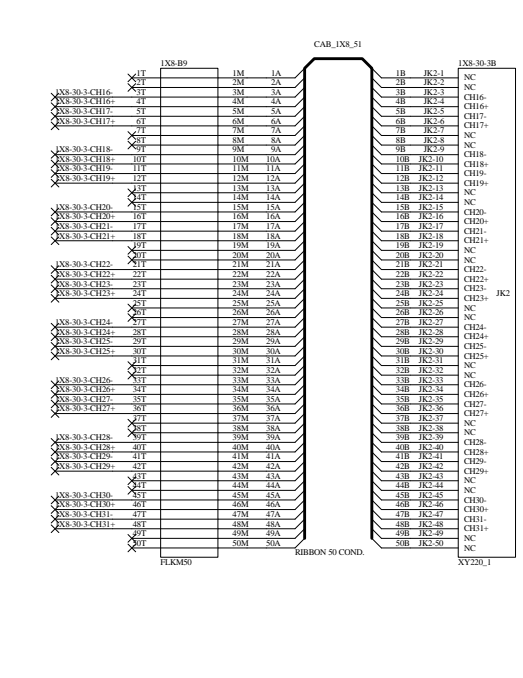
3113 Card 3

RM, BS Opt Lev Int Sheet 8

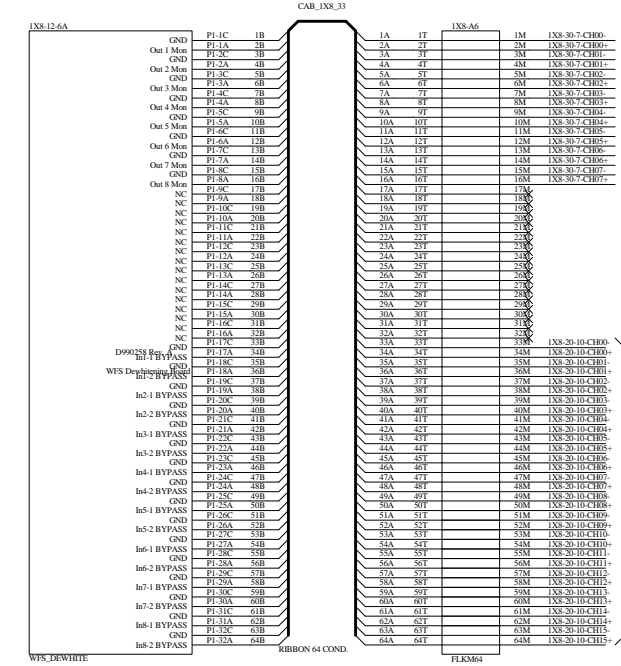
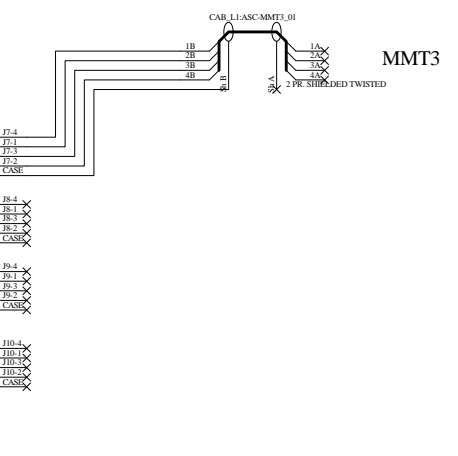
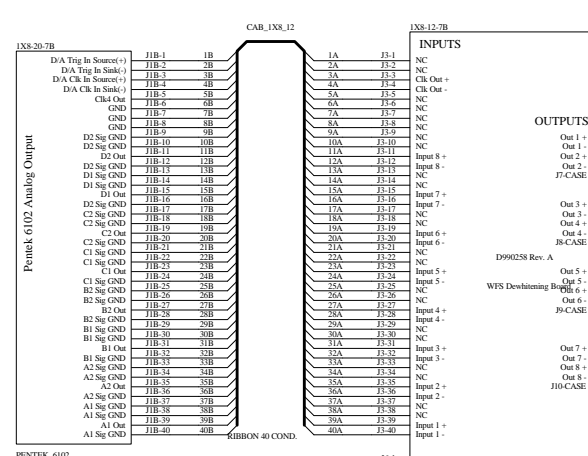
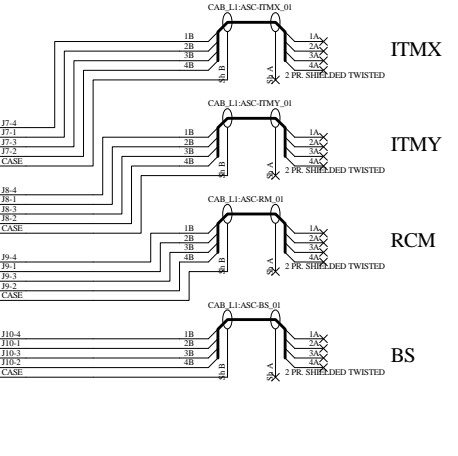
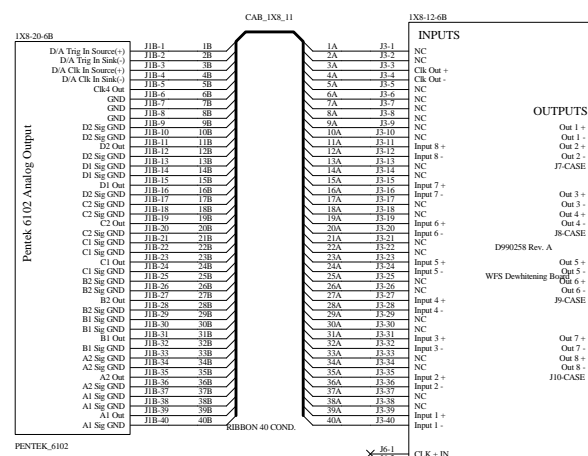


3113 Card 3

ITMX, ITMY Opt Lev Int Sheet 8

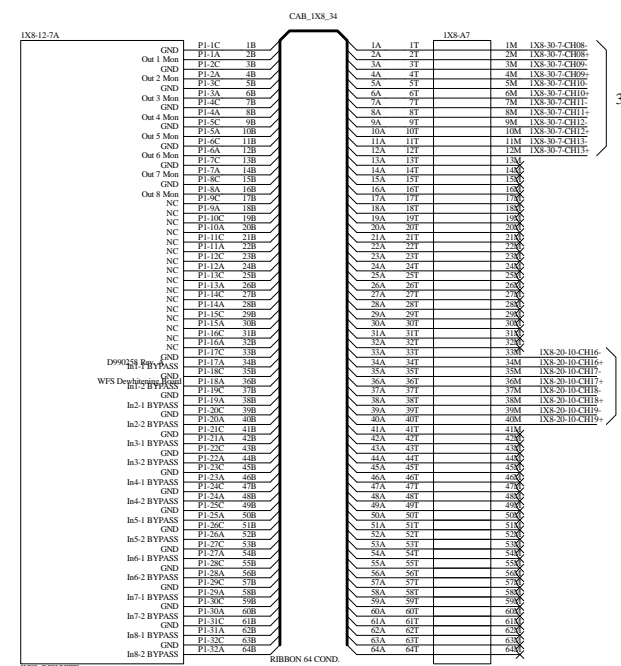


XY220 Card 1



3113 Sh. 9

XY220 Sh. 9



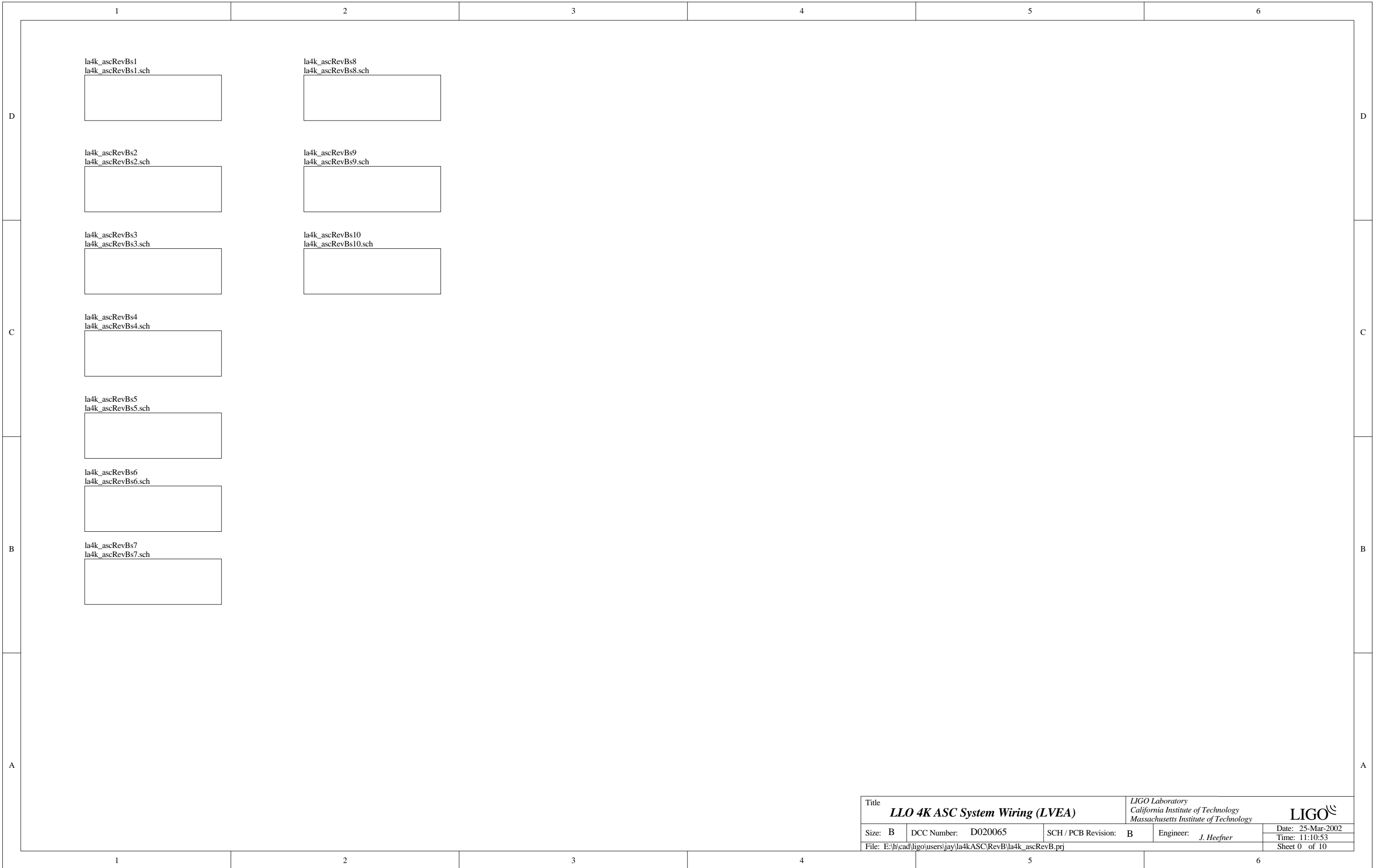
3113 Sh. 9

XY220 Sh. 9

Rev	Description	Date
A	Initial Release	2/02
B	As built changes to slot numbers and cable numbers	3/02

Title: **LLO 4K ASC System Wiring**
 LIGO Laboratory
 California Institute of Technology
 Massachusetts Institute of Technology
 LIGO

Size: D DCC Number: D020065 SCH / PCB Revision: B Engineer: J. Heffner Date: 25-Mar-2002
 File: E:\llo\asc\asc\llo_4k_asc_sys_wiring_b01.dwg Time: 11:31:27
 Sheet 10 of 10



Title		LLO 4K ASC System Wiring (LVEA)		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO	
Size: B	DCC Number: D020065	SCH / PCB Revision: B	Engineer: J. Heefner	Date: 25-Mar-2002	Time: 11:10:53		
File: E:\h\cad\ligo\users\jay\la4kASC\RevB\la4k_ascRevB.prj				Sheet 0 of 10			